American Recovery and Reinvestment Act of 2009

Use of IEC 61850-90-5 to Transmit Synchrophasor Information According to IEEE 37.118
NASPI Tutorial, October 16, 2012
August 2014 Update

NASPI Synchrophasor Technical Report
Introduction and overview

This report offers an overview of the technical interoperability standards applicable to synchrophasor technology and substation automation, with a principal focus on IEC 61850, the substation automation standard. The IEC, International Electrotechnical Commission, is the world’s leading organization that prepares and publishes International Standards for all electrical, electronic and related technologies. Synchrophasor communications are addressed in IEC 61850-90-5\(^1\), which was adopted in 2012. But it is important to understand the broader framework established in the suite of IEC 61850 standards to understand and use the synchrophasor-specific standard effectively.

The North American SynchroPhasor Initiative (NASPI) held a tutorial on IEC 61850 in October 2012. This report summarizes that workshop and explains briefly how this standard relates to other synchrophasor technical standards.

NASPI is a collaboration between the North American electric industry (utilities, grid operators, vendors and consultants), the U.S. Department of Energy, the Electric Power Research Institute, the North American Electric Reliability Corporation, and academics, to advance and accelerate the development and use of synchrophasor technology for grid reliability and efficiency. The material attached was produced for one of a series of NASPI technical workshops intended to educate and document the stakeholder community on the state of the art for key synchrophasor technology issues.

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\(^1\) The formal title of Standard IEC 61850 is, “Power Utility Automation”; IEC 61850-90-5 is, “Use of IEC 61850 to transmit synchrophasor information according to IEEE C37.118”.

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The Department of Energy and synchrophasor technology

Synchrophasor technology was developed thanks to early research efforts by the U.S. Department of Energy and Bonneville Power Administration in the 1990s. With recognition that synchrophasor technology -- high-speed, wide-area, time-synchronized grid monitoring and sophisticated analysis -- could become a foundational element of grid modernization for transmission system, the Department continued and expanded its investment and industry partnership in the areas of synchrophasor measurement devices, communications, applications, measurements, and technical interoperability standards.

In 2009, the Department committed a total of $412 million of funds from the American Recovery & Reinvestment Act of 2009 to twelve Smart Grid Investment Grants and one Smart Grid Demonstration Project that implemented and tested synchrophasor technology using matching private funds. While some of the ARRA funds were spent on other transmission assets, in aggregate over $328 million of federal and matching private investment was spent on synchrophasor technology and related communications networks.

Additionally, DOE has funded significant technical assistance for NASPI and synchrophasor advancement through the National Laboratories and the National Institute for Standards & Technology, including funding to accelerate smart grid technical standards development -- including those for synchrophasor technology.

NASPI serves as a forum for information-sharing and problem-solving among the synchrophasor projects and stakeholders. Much of the work and insights reflected in this technical workshop were enabled by individuals and companies funded by DOE’s ongoing research and development projects and the ARRA investments. Thus it is appropriate to recognize the insights and work product documented in this workshop and technical report as one of many consequences and work products resulting from the federal Smart Grid investments. Therefore, the Department joins NASPI in re-releasing this material to the smart grid community to document additional impacts and value realized from the federal Smart Grid investments in synchrophasor technology.
Introduction and overview

This report offers an overview of the technical interoperability standards applicable to synchrophasor technology and substation automation, with a principal focus on IEC 61850, the substation automation standard. The IEC, International Electrotechnical Commission, is the world’s leading organization that prepares and publishes International Standards for all electrical, electronic and related technologies. Synchrophasor communications are addressed in IEC 61850-90-5, which was adopted in 2012. But it is important to understand the broader framework established in the suite of IEC 61850 standards to understand and use the synchrophasor-specific standard effectively.

IEC 61850 and the NASPI tutorial

In November 2012, the North American SynchroPhasor Initiative held a tutorial led by expert members of the synchrophasor standards update group to explain the benefits of using IEC 61850 and some of the technical details behind the standard’s structure and use. This document summarizes the presentations and comments made during that tutorial.

The expert presenters at this tutorial were:

- Vahid Madani (PG&E)
- Ron Farquharson (Enernex)
- Mark Adamiak (GE Energy)
- Ralph Mackiewicz (Sisco)
- Herbert Falk (Sisco)
- Christoph Brunner (it4power)
- Farnoosh Rahmatian (Quanta Technology)

The tutorial agenda and documents presented are posted at https://www.naspi.org/techworkshops and attached behind this summary. The full tutorial was videotaped and can be accessed at the same link.

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2 The formal title of Standard IEC 61850 is, “Power Utility Automation”; IEC 61850-90-5 is, “Use of IEC 61850 to transmit synchrophasor information according to IEEE C37.118”.
Synchrophasor technology and standards

Synchrophasor technology monitors electrical waves on the grid and synchronizes those measurements to a common time source. It characterizes grid conditions by calculating phasors (amplitude and phase) at a rate of 30 or 60 samples per second (1,800 samples per minute, compared to SCADA’s 12 to 15 samples per minute). Phasor measurement units (PMUs)\(^3\) measure or calculate frequency, voltage, current and phase angles, time-synchronized across the grid. These measurements, taken from diverse points across the grid, allow unprecedented visibility and better situational awareness of real-time grid conditions. Analysts can use synchrophasor data to analyze dynamic grid events and to improve static and dynamic models of grid assets such as generators.

Although synchrophasor concepts were developed in 1893, the first PMU was developed in 1988 and the first commercial PMU became available in 1992. The first technical standard developed to describe PMUs and guide synchrophasor measurements was IEEE 1344-1995.\(^4\) It was superseded by IEEE C37.118-2005, which covered synchrophasor measurement, quantification and test specifications and defined PMU data transmission formats. IEEE, the Institute of Electrical and Electronics Engineers, is the world’s largest professional association dedicated to advancing technological innovation and excellence for the benefit of humanity.

In 1995, the International Electrotechnical Commission began work on IEC 61850 as a standard for design of electrical substation automation systems and applications, to provide a single communication protocol for the interoperable storage, delivery, and exchange of diverse data across a substation. IEC 61850 (edition 1) became an International Standard in 2004. IEC 61850-90-5 is the standard that characterizes how the 61850 data protocols are applied to synchrophasor communications: it applies to communications from PMUs to Phasor Data Concentrators (PDCs) and Wide Area Monitoring, Protection and Control systems, and between control center applications.

In 2009, the IEC and IEEE created a joint task force to develop methodologies and agreements for coordinated updates to both standards. This effort yielded modifications and updates to both IEC 61850 and IEEE C37.118. The updated IEC 61850-90-5 standard was completed in October 2011 and published in May 2012.

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\(^3\) The term PMU is used here to include a suite of devices that include PMU functionality and are used as PMUs, including digital relays upgraded to perform as PMUs, digital fault recorders, and FNET monitors.

\(^4\) There are two relevant technical standards bodies for synchrophasor technology, the International Electrotechnical Commission (IEC) and the IEEE Power and Energy Society. The IEEE and IEC organizations have formal processes for cooperation and coordination, including the possibility that one organization adopts the other’s adopted standard, and the possibility of joint development of new or revised standards. (See IEC and IEEE “Guide to IEC/IEEE Cooperation,” retrieved June 14, 2014 at http://standards.ieee.org/develop/intl/iec_ieee_coop.pdf.)
In December 2011, IEEE adopted modifications and updates to IEEE C37-118 to add
more precision and extend the measurement from static to dynamic power system
conditions:

- **Synchrophasor Measurements** -- IEEE Std. C37.118.1-2011 is the Standard for
  Synchrophasor Measurements for Power Systems; it defines the precise
  requirements for PMU steady-state and dynamic performance, including how
  various synchrophasor elements should be measured (including acceptable error
  ranges), computed and calculated (e.g., estimation algorithms and corrections). It
  defines a PMU as “a device that produces synchronized measurements of phasor
  (i.e., its amplitude and phase), frequency, ROCOF (Rate of Change of Frequency)
  from voltage and/or current signals based on a common time source that typically
  is the one provided by the Global Positioning system (GPS).”

- **Synchrophasor Data Transfer** -- IEEE Std. C37.118.2-2011 is the Standard for
  SynchroPhasor Data Transfer for Power Systems; it covers communications
  standards for the formatting and delivery of synchrophasor data.

IEC 61850-90-5 and IEEE C37.118.1-2011 and 37.118.2-2011 differ in scope and
content:

While IEEE C37.118.1 is “the global standard for defining the
measurement technology for synchrophasors, … IEEE C37.118.2 is the
IEEE protocol to address current system requirements, enabling IEC TR
61850-90-5 to be the basis for a more scalable, and secure, protocol to
meet NASPIIet requirements. IEC TR 61850-90-5 has normative
references to IEEE C37.118.1 as the method for measuring
synchrophasors.”

IEC 61850-90-5 lays out a path for synchrophasor communications to migrate from the
C37.118 architecture to the 61850 architecture, which supports faster sampling and data
transmission rates and better cyber-security, and thus is better suited for system
protection and control applications.

**Vahid Madani (PG&E)** opened the tutorial with an overview of the tutorial objectives --
to understand the benefits of IEC 61850, its underlying technology, life cycle impact,
how it integrates with other standards, installation examples, and deployment success
factors. Madani noted that before IEC 61850 was available, several different
communications protocols were used within electrical substations worldwide, and it was
difficult and costly to make those protocols work together and integrate devices
effectively. Thus the IEC standards community began working to develop a new
communications protocol for data used within electric substations, to enable
standardization, interoperability and ease of use for substation automation networks and

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5 IEEE Standard C37.118.1-2011 has since been superseded by IEEE C37.118.1a-2014. This can
7 Ibid.
Along the way, the IEC integrated the work of EPRI’s Utility Communications Architecture (UCA) effort to produce the IEC 61850 architecture and protocols.

Ron Farquharson (EnerNex) reviewed the history of IEC 61850. It was originally intended to address substation automation, but has been expanded to address many of the automation systems used across the electric utility transmission and substation domains. It supports traditional protection and control functions, but is intended to enable new, advanced capabilities and integrate with enterprise applications that use the Common Information Model (an object-oriented information model of the power system that was adopted as part of IEC 61970). The standard uses object-oriented programming (a computational semantics approach for programming languages developed in the 1990s) with object models for different substation devices, to simplify and streamline power system component and communications design.

IEC 61850 is a multi-part standard covering a number of components; the organization of those components within the standard is laid out in Figure 1. As indicated within the figure, communications protocols relevant to synchrophasor measurements are contained in Part 9 of the standard (hence the naming suffix, 61850-90-5).

Figure 1 -- IEC 61850 Document Set

Farquharson itemized a number of benefits and impacts from the use of IEC 61850:

- Because it standardizes communications protocols and interfaces, it will make substation equipment interoperable and thus lower equipment acquisition and installation costs.
The availability of standard object models will lower substation engineering and design costs by enabling automated system engineering tools and processes and new substation designs. By enabling in-substation wire-less (no copper wires) communications, it will lower substation construction and commissioning costs by reducing or eliminating relay-to-relay wiring. It improves asset security because IEC 61850 incorporates the comprehensive IEC 62351 cyber-security standard. It can transmit waveform samples in real-time, enabling high-speed data services that can support real-time protection and control actions (e.g., tripping over the LAN). It enables the use of single measurements (e.g., CT and PT transducer signals) by many users or devices and applications (Multi-cast as well as Unicast), which enhances efficiency and redundancy and reduces equipment connection and wiring costs. Devices under 61850 have standard object naming conventions and are self-describing and discoverable by other 61850 devices and controllers, which reduces the cost and time required for design, specification, configuration, testing, commissioning and maintenance. IEC 61850 guidance for testing, verification and quality assurance of devices will expedite completion of compliance and certification processes, which will lower buyers’ costs for requirements specifications and increase the availability of 61850-compliant equipment for transmission and distribution substation and field deployment.

Early experience with IEC 61850 by U.S. and other utilities indicates that using IEC 61850 for substation automation does in fact yield measurable engineering, operations and maintenance benefits due to the factors listed above.

Ralph Mackiewicz (Sisco) outlined the technical elements of IEC 61850. Mackiewicz stressed that the point of standards is to make diverse things work together in a cooperative and distributed fashion. Technical standards facilitate interoperability, defined as, “the ability of computer systems to exchange information with other systems and to cooperatively implement a useful process or function for the system owner or user,” particularly through protocols that minimize differences at the interfaces between hardware devices. IEC 61850 (and similar standards) address both interoperability between devices and integration between applications, using protocols that span the power system with common specifications for data formats, meaning and behavior.

Mackiewicz reviewed the IEC 61850 object model (as illustrated in Figure 2) and explained the relationship between the CIM and 61850 information models and modeling tools. He stressed that 61850 and the model-driven approach dramatically improves the productivity of planning, design, device selection, engineering and configuration, implementation, wiring, testing and maintenance of automated power systems.
Figure 2 -- IEC 61850 Object Model
(Source: Ralph Mackiewicz & Herbert Falk, “Insight in IEC 61850 -- What is it?”
NASPI IEC 61850 tutorial, October 16, 2012)

Figure 3 shows an object name consistent with the above format.

Figure 3 -- Anatomy of an IEC 61850-8-1 object name
(Source: Mark Adamiak & Drew Baigent (GE Digital Energy) and Ralph Mackiewicz (SISCO),
“IEC 61850 Communication Networks and Systems in Substations: An Overview for Users,”

Herb Falk (Sisco) explained how IEC 61850 works within substations (Figure 4) and
over networks (Figure 5) and reviewed various IEC 61850 profiles to show the ways that
networks could be configured and data managed and routed under the 61850 architecture.
Figure 4 -- IEC 61850 Substation Architecture
(Source: Ralph Mackiewicz & Herbert Falk, “Insight in IEC 61850 -- What is it?”
NASPI IEC 61850 tutorial, p. 72, October 16, 2012)

Figure 5 -- IEC 61850 Profiles
(Source -- Ralph Mackiewicz & Herbert Falk, “Insight in IEC 61850 -- What is it?”
NASPI IEC 61850 tutorial, p. 64, October 16, 2012)
Falk also reviewed the communications elements contained within the various sections of IEC 61850, and explained the role of Logical Nodes -- the grouping of data and services relating to a specific power system function, with attributes defined by a Common Data Class. Power system objects such as breakers have data attributes such as control, configuration, measurement and reporting, and those data attributes have Functional Constraints specific to that piece of data. Falk reviewed many of the functional constraints and value options, and some of the logical nodes relevant for substation objects. (See Figure 6)

**Figure 6 -- IEC 61850 Device Structure**
(Source -- Ralph Mackiewicz & Herbert Falk, “Insight in IEC 61850 -- What is it?” NASPI IEC 61850 tutorial, p. 75, October 16, 2012)

Falk explained the IEC 61850 Service Model, which offers several messaging options:

- **GOOSE** -- Generic Object Oriented Substation Events messages are event-based. They group data into a dataset and transmit it in Ethernet data packets within four milliseconds. GOOSE messages are multi-cast (sent on a publisher-subscriber basis from one source to numerous pre-authorized recipients) and are retransmitted for redundancy (until the next event occurs).
- **GSSE** -- Generic Substation State Events convey status data in a simpler format than GOOSE messages.
- **SV** -- Sampled Value (or Sampled Measured Value) messages contain PT and CT measurements that are streamed in real time.
- **ACSI** (Abstract Communications Service Interface).
Last, he reviewed Substation Configuration Language (SCL), an XML-based language that describes the substation automation system, switchyard, and Intelligent Electronic Devices (IEDs) and the relationships and configuration of IEDs within SCL.

**Christoph Brunner** (it4power) reviewed some of the early applications of IEC 61850 in substations. These include two substations in Switzerland in 2004, and substations in Bangalore and Australia. He also reviewed several ways that IEC 61850 has been used for specific applications, including under-frequency load shedding, reverse blocking, and other system protection schemes.

Brunner outlined the extensive amount of work and technical reports that the IEC Working Groups have undertaken to extend IEC 61850 to additional types of power system components (such as FACTS devices and gas turbines), communication relationships (as between substations and the control center), and into distribution automation and distributed energy resources. He closed with a review of communication redundancy options.

**Mark Adamiak** (GE Digital Energy) addressed GOOSE and SV messaging. He opened by comparing unicast messaging (sending one message to one subscriber) to multicast (sending one message to intended recipients) and broadcasting (sending one message to all recipients with no restrictions). The NASPInet vision anticipates that encrypted synchrophasor data will be shared through multicast with a secure publish-subscribe model. (Figure 7) To this end, an IEC committee recently approved a Technical Report on how to use IEC 61850 to transmit synchrophasor information according to IEEE C37.118, which goes far to integrate the two standards.

**Figure 7 -- NASPInet architecture vision**
Adamiak reviewed the IEC 90-5 data model (Figure 8) to explain how IEC 61850-90-5 messages in the GOOSE and SV formats can be used for NASPInet data transfers. He offered detail on how these data packets could be structured and mapped, with appropriate security provisions for those messages.

**Figure 8 -- IEC 90-5 Data Model**

Mark Adamiak (GE Digital Energy) and Farnoosh Rahmatian (Quanta Technology) then discussed how IEEE 1588, the standard for Precision Time Protocol (PTP) over Ethernet, is being used for the time synchronization that makes grid measurements into valuable synchrophasors. IEEE 1588 applies to data synchronized in sub-microsecond time spans, and provides a messaging system to identify and guide master and slave clock performances and how the time synch calculations should be made and applied. (See Figure 9) Other related timing functions include GPS and IRIG-B.
As the synchrophasor message travels from the point of measurement and calculation and initial time-stamping at the PMU through PDCs, the communications network, and up to the application software that uses it, the message can experience latency and fluctuation. PG&E’s synchrophasor project tested the effectiveness of the 1588, GPS and IRIG-B methods and the impact of these latency delays, using PG&E’s Synchrophasor Proof of Concept facility. These tests revealed that clock performance varies in terms of accuracy and drift over time, and that delays in network communications can cause delays in the Transparent Clock.

Vahid Madani (PG&E) and Farnoosh Rahmatian (Quanta Technology) closed the tutorial with examples of interoperability issues and tests relating to IEC 61850. They explained that applicable technical standards and a compliance regime are necessary but not sufficient to achieve interoperability between devices -- true interoperability usually requires implementation agreements among device developers and manufacturers, plus testing to verify that true inter-operation has been achieved. Although there is a plan to develop certification for IEC 61850-compliant devices, such certification alone will not assure that 61850-conformant devices are interoperable.

Switching to design requirements for an IEC 61850 substation, Madani and Rahmatian discussed system design requirements and criteria, including the advantages of networked communications versus local control wiring for wide-area protection and control. They
pointed out that Ethernet controls and use of several emerging technologies can enhance system flexibility and accuracy, offer insights for asset maintenance and event analysis, and speed data access and process controls. But these require continuing adoption of substation automation (under IEC 61850 specifications), further development of industry tools and applications (as for maintenance and device performance tracking), conformance certification, extensive process changes for substation automation and protection design, and extensive training of engineers, vendors and practitioners on how to use IEC 61850 concepts, devices and networks effectively.

Madani and Rahmatian also offered recommendations for how to conduct interoperability testing for PMU to PDC communications, and a comparison of data transmission using C37.118 UDP versus IEC 61850 Unicast and Multicast. They advised that it is valuable to have a collaborative testing environment between vendors and users, and the importance of developing efficient configuration and testing tools.

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Sources and references


Apostolov, Alex, “IEC 61850-90-5”, NASPI PTTT, February 2011

Brunner, Christophe, “Synchrophasor Data and IEC 61850”, NASPI PTTT, February 2010


IEC Standard 61850-90-5, “Power Utility Automation”


The NASPI community has invested significant time and effort in the development of IEC technical interoperability standard 61850-90.5, the technical interoperability standard newly adopted by the International Electrotechnical Commission as part of the reference architecture for electric power systems. This tutorial will give attendees a clear and practical understanding of what this technical standard entails, how it be used for PMU measurements and data communications, why technical interoperability standards matter, and what comes next. The tutorial will be taught by two IEEE Fellows and associated experts who developed the new standard and are guiding the electric industry in its implementation.

There will be no registration fee for this tutorial. The meeting registration link is at http://www.nerc.net/meetingregistrations/Committee.aspx?meetingdate=10/16/2012&meetingtype=NASPI%20IEC.

NASPI has secured a block of rooms at the Westin Buckhead Hotel for the nights of October 15 through 18 for those who wish to attend this tutorial or the NASPI Work Group meeting, which will be held on October 17 and 18. The hotel reservation link is at https://www.starwoodmeeting.com/StarGroupsWeb/booking/reservation?id=1112307682&key=5A40.

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<td>Alison Silverstein (NASPI)</td>
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<td>Tutorial objectives and a brief history of IEC 61850</td>
<td>Vahid Madani (PG&amp;E) Ron Farquharson (Enernex)</td>
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<td>IEC 61850 benefits and overview of NIST standardization process</td>
<td>Mark Adamiak (GE Energy) Ron Farquharson (Enernex)</td>
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<td>Insight in IEC 61850 - What is it?</td>
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<td>IEC 61850 Applications with real-life examples</td>
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<td>Mark Adamiak (GE Energy)</td>
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<td>Brief overview of IEEE 1588 test results – Concept and practical examples and lessons learned</td>
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NASPI Tutorial on IEC 61850 Introductions

Tuesday October 16, 2012

Vahid Madani
Pacific Gas and Electric Co.
Tutorial Objective

- Benefits and Impact of IEC 61850
- Understanding of technology behind IEC 61850
- Life cycle impact of IEC 61850
- Integration with other standards and interoperability
- Installation examples - North America and Internationally
- Key deployment success factors and deployment roadmaps
Tutorial Overview and Outline

• Tutorial objectives and brief history of IEC 61850 – Madani / Farquharson
• IEC 61850 benefits and overview of NIST standardization process – Adamiak / Farquharson
• Insight in IEC 61850 - What is it? – Mackiewicz / Falk
  a) What is IEC 61850 and Its Relationship to other IEC Standards
  b) Impact of IEC 61850 on the Engineering Processes of SCADA, Automation, Protection
  c) IEC 61850 Profiles
  d) IEC 61850 Modeling
     i. Device and Object Models
     ii. Service/Behavior Models
  e) Substation Configuration Language
• Q & A Session – Novosel

Break - 15 Minutes
• IEC 61850 Applications with real-life examples, Recent Industry Activities Brunner / Madani
• IEC 61850-90-5, including interaction with IEEE 37.118 M. Adamiak
• Brief overview of IEEE 1588 test results – Concept and practical examples and lessons learned - Rahmatian / Adamiak
• Interoperability issues, tests, and practical examples - Madani / Rahmatian
• Q&A session – Novosel

Two clarification type qualification questions from each presenter immediately after the presentation. Additional questions will be deferred to the panel session.
Why IEC 61850?
Harmonization, Standardization, Ease of Use

Too Expensive & Complex
IEC 61850

• Necessity to simplify & standardize substation design, construction, commissioning, operation and maintenance

Driven By
• Utilities through UCA & IEC standard committees
IEC 61850 - Communication networks and systems in substations

Why it is real:

- Driven by Utilities to meet a need
- Finally see the “Promised LAN” (…and we don’t anticipate taking 40 years to get there…)
- It works and is in the market (after many years)
- Fits and supports today’s Power System Technical and business drivers – Makes a business case
- Networking dominates the utility landscape
- Time for paradigm Shift
  - If you are not on-board, you will be soon
  - Start Developing the foundations & standards for application in your company
    » Timing is right
    » Better get involved sooner
Origin and History of IEC 61850

Ron Farquharson, Principal Consultant, EnerNex
IEC 61850’s role in the “Big Picture”

- Identified in the first batch of interoperability standards key to Smart Grid deployment by NIST
- Initial focus was substation automation (Edition 1)
- Scope is now expanded to address many of the automation systems utilized in the electric utility transmission and distribution domains
- Supports traditional protection and control functions while enabling new advanced capabilities
- Future integration with enterprise applications using CIM (Common Information Model)
IEC61850 Interface Model

- Control Center
- Technical Services
- Station Bus
- Station Level
- Bay Level
- Process Level
- HV Equipment
- Remote Protection
- Process Interface
- Protection - Control
- Fct A
- Fct B
- Process Interface
- Protection - Control
IEC 61850 Document Set

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IEC 61850

Object Modeling: Visualize a Substation

- IEC 61850-7-2 Services
- Logical device (Bay)
- Virtualisation
- Real devices in any substation

TCP/IP Network → MMS → Mapping

SCSM 61850-8-1

61850-7-4 logical node (circuit breaker)

61850-7-4 data (Position)

61850-6 configuration file
Sponsored by Electrical Power Research Institute (EPRI)
Early work by GE, KEMA, others
EPRI RP3599 became UCA 1.0 in 1990 - no TCP/IP
Many pilot projects, no consensus
American Electric Power initiative brought focus
UCA 2.0 became the core technology in IEC 61850 released as Edition 1 in 2003
EPRI considered that UCA included the popular Inter-Control-Center Protocol (ICCP) a separate standard: Telecontrol Application Service Element (TASE.2), IEC 60870-6
IEC 61850

Origin and History - 2

- UCA International Users Group founded in 2003
- IEC 61850 Edition 2 – releases started in 2010
- New technical reports for synchrophasor data, condition monitoring, communications outside the substation (other stations, control centers etc)
- New standards for DER, hydro, wind, distribution automation, electric vehicle charging
- Standards for mapping to other protocols:
  - IEC 60870-5-104
  - IEEE 1815 – 2010 (DNP3) - pending
- Cyber security addressed by IEC 62351
IEC 61850

Origin and History - 3

1994
EPRI
IEEE, USI

1996
IEC TC57

1997 ↔ 2003

61850

UCA 2.0

IEEE TR 1550:1999

“UCA & 61850 for Dummies.” – Douglas Proudfoot
UCA International User’s Group (UCAIug)

- Founded in 2003
- Key Committees:
  - Technical Oversight Committee
  - CIMug (CIM Sub Committee)
  - IEC61850ug (IEC61850 Sub Committee)
  - OSGug (Open Smart Grid Sub Committee)
  - Harmonization Sub Committee
  - Technical Publications Sub Committee
  - Testing Sub Committee
Rationale and Benefits for IEC 61850 and NIST Catalog of Standards

Ron Farquharson, Principal Consultant, EnerNex
Why IEC 61850?

Example Benefits by Stakeholder

- Utility Decision Maker
  - Lower capital costs
  - Improved engineering efficiency
  - Benefits of data modeling extend into the utility enterprise

- Utility Engineer
  - New tools and options for designing systems to meet the evolving requirements of a smart grid world
  - Process improvements

- Regulator
  - High benefit-cost ratio (BCR)

- Vendor
  - Narrower focus on product communication features - more focus on innovation, features, benefits, cost
So Why all the Excitement?

Part of a broad scope, world-wide electric power interoperability effort for devices & systems:

- Industry consensus object models for power system devices
- Self-description and structured meta data
- Publish/subscribe services
- Fast data services for protection/control (eg tripping over LAN)
- Transmitting waveform (synchrophasor) samples in real-time
Part of a broad scope, world-wide electric power interoperability effort for devices & systems (cont’d):

- LAN-Based Time Synchronization
- Cyber security (IEC 62351)
- Substation Configuration Language
- Support for automated system engineering tools and processes
- Testing, verification, and quality assurance processes
Why IEC 61850?

Fast Peer-to-Peer Communication

- Generic Object-Oriented Substation Event (GOOSE)
- Intended to replace relay-to-relay wiring
- Each device multi-casts (one to many) a selected set of data
- Assumes the message will not get through
- Retransmits immediately, exponential back-off
- Periodic retransmission
Why IEC 61850?

**Sampled Values on the LAN**

- Enables a single measurement and sharing with multiple devices and applications
- Separates sampling physically from:
  - Measurement
  - Metering
  - Calculation
- More flexibility in measurement
  - Any device may measure any circuit
- Used for synchrophasor measurement (TR 61850-90-5)
- Gigabit Ethernet will likely be required
What are the Benefits of IEC 61850?

- GOOSE (high performance) messaging for inter-relay communications can reduce hard wiring and/or enable functions not possible with hard wires. Saves $$ in engineering, wiring and maintenance.

- Enhanced communications services enable self-describing devices and automatic object discovery. Saves $$ in configuration, testing, commissioning and maintenance.

- Sampled measured value messaging enables sharing of transducer (CT/PT) signals. Saves $$ by reducing equipment, wiring and connection costs.

- Comprehensive cyber security solution (IEC 62351)

Contributions: Mark Adamiak, GE
What are the Benefits of IEC 61850?

- Standardized configuration file formats enables exchange of device configuration and auto-configuration capabilities. Supports a range of tools. Saves $$ in design, specification, configuration, testing, commissioning and maintenance.

- Industry standard object naming conventions with power system context eliminates device dependencies and tag mapping. Saves $$ in configuration, testing, commissioning and maintenance.

- Significant potential in process improvement!!
(They are in the process of adopting IEC 61850 for their next generation substation automation systems)

Successful implementation of “IEC 61850” will result in measurable engineering, operations and maintenance benefits through cost reduction, reliability and safety.

The primary benefits arise the use of an open system such as the IEC 61850 standard for substation communications and include:

- Cost Savings due to process improvement
- Enhanced Security
- Configuration Management (auto-configuration)
- Interoperability – including future integration with distribution automation and field area networks.
PAP-13 SGIP Activities - 1

- IEEE Std C37.238-2011 – now in the Catalog of Standards
  - Full document set completed and posted on CoS TWiki
  - Successful reviews by the CSWG and SGAC
  - IEEE Std C37.238-2011 approved for Catalog of Standards – October 18, 2011

- Moving forward:
  - T & D DEWG to address time synchronization compatibility issues that were identified as part of our PAP13 work.
  - Need to address (per the CSWG review) cyber security deficiencies with the existing IEEE 1588 standard.
PAP-13 SGiP Activities - 2

- IEC TR 61850-90-5 – now in the Catalog of Standards:
  - Full document set completed and posted on CoS TWiki
  - Successful reviews by the CSWG and SGAC
  - IEC TR 61850-90-5 approved for Catalog of Standards – September 14, 2012

- Moving forward:
  - Technical Report means that the content is gradually adopted into the next editions of the various parts of IEC 61850.
  - A task group is developing an Implementation Guide to address (e.g.) mapping between IEEE C37.118.2 and IEC 61850.
  - IEEE PSRC meeting has formed a new Task Force to determine whether a formal IEEE standard should be developed for the Implementation Agreement
Contact Information

- Ron Farquharson
  - ron@ernex.com

- http://www.ernex.com/
The New GOOSE & SV

Mark Adamiak
GE Digital Energy
Functional Requirement: One message to multiple subscribers

Unicast solution – limited on connection
Definition of Terms: Broadcast

Router Duplicates Packet to ALL Locations
Multicast Solution:

One Message Only Sent to Intended Recipients
# Multicast Addresses

<table>
<thead>
<tr>
<th>Class D Address</th>
<th>Purpose</th>
</tr>
</thead>
<tbody>
<tr>
<td>224.0.0.1</td>
<td>All hosts on a subnet</td>
</tr>
<tr>
<td>224.0.0.2</td>
<td>All routers on a subnet</td>
</tr>
<tr>
<td>224.0.0.4</td>
<td>All DVMRP routers</td>
</tr>
<tr>
<td>224.0.0.5</td>
<td>All MOSPF routers</td>
</tr>
<tr>
<td>224.0.0.9</td>
<td>Routing Information Protocol (RIP)-Version 2</td>
</tr>
<tr>
<td>224.0.1.1</td>
<td>Network Time Protocol (NTP)</td>
</tr>
<tr>
<td>224.0.1.2</td>
<td>SGI Dogfight</td>
</tr>
<tr>
<td>224.0.1.7</td>
<td>Audio news</td>
</tr>
<tr>
<td>224.0.1.11</td>
<td>IETF audio</td>
</tr>
<tr>
<td>224.0.1.12</td>
<td>IETF video</td>
</tr>
<tr>
<td>224.0.0.13</td>
<td>Protocol Independent Multicasting (PIM₁)</td>
</tr>
</tbody>
</table>

1 – CISCO Recommended
Gaps in C37.118

- No defined security
  - Requires an external solution
  - Multicast and associated security not defined
- Dataset Configuration tools are vendor specific
- No standard data names
- Limitations on dataset name length
- Lack of Negative response from the server
  - Some consider this a feature......
North American SynchroPhasor Initiative Network - NASPInet Vision

![Diagram of NASPInet Vision](image-url)
Mapping Synchrophasors into GOOSE

GOOSE/SV message

GOOSE
- No IP Address
- No Transport
  - UDP
  - TCP

Router w/ UDP/IP

Bit Bucket

Routed GOOSE/SV

Requires Router configuration

GOOSE after router
Communication NEEDS: Networked Publish/Subscribe Message

- Should be routable
  - Multicast to reach multiple subscribers
- Message should be authenticated
- Message should be able to be encrypted
- Should accommodate large message size
- Should manage the Authentication and Encryption key
Committee Approved Technical Report:

IEC 61850:
COMMUNICATION NETWORKS AND
SYSTEMS FOR
POWER UTILITY AUTOMATION –

Part 90-5: Use of IEC 61850 to transmit synchrophasor information according to IEEE C37.118
Mark’s Proposed name for IEC 90-5: Networked Object Oriented Substation Event

The NOOSE!
But people got hung up on the idea... So we have:

• R-GOOSE (for Routed GOOSE)
  • For routing of Event Data

And

• R-SV (for Routed Sample Values)
  • For routing periodic data
IEC 90-5 Data Model

SPDU:
Session Protocol Data Unit

Total Max Size: 65535 bytes
Supports Multiple non-time-aligned PMU datasets
IEC 61850 90-5 Networked GOOSE/SV

Potential Solution for NASPINEt
Payload

- Consists of Multiple IEC 61850-9-2 Protocol Data Units (PDUs)
- PDU consists of:
  - Header
  - Dataset

An Implementation Agreement is proposed to “agree” to the items in the Header and Dataset
Implementation Agreement: Header

- Multicast Sample Value ID – MSVID
  - `<name>`-`<IDCode>`-`<Class>`
  - IDCode inherited from C37.118 (PMU or PDC)
  - Class = P, M, or N (for none)
  - All data in a given PDU shall be of the same Class

- RefrTim – Mandatory
  - Synchrophasor TimeStamp per C37.118
  - Same Time Stamp as in C37.118 (SoC+FoS+TQ)
  - $\text{TimeBase} = 2^{24} = 16,777,216$
  - $\text{TQ} = \text{C37.118 TQ}$
Implementation Agreement
Dataset inclusions

• STAT word
  • 16 bit Unsigned Integer
  • Semantics from C37.118

• Synchrophasors Frequency and ROCOF to be included

• Synchrophasors

• Analog Values
Reliable Transport through Repeat

ASDU1 – Oldest data
ASDUn – Newest data
n is a user-settable parameter
### Internet Protocol Priority via Differentiated Services byte

<table>
<thead>
<tr>
<th>Bit #</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>4</td>
<td>1</td>
</tr>
<tr>
<td>5</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>0</td>
</tr>
<tr>
<td>7</td>
<td>0</td>
</tr>
</tbody>
</table>

- **DSCP** - Differentiated Service Code Point
  - set for Expedited Forwarding (0x2E)
- **ECN** – Explicit Congestion Notification
  - Set by the routers if enabled
- **Overall byte value**: 0xB8
Mapping of C37.118 FoS to 61850

\[
\text{FractionOfSecond}_{61850} = \frac{\text{FractionOfSecond}_{C37.118}}{C37.118\_Time\_BASE} \times 16,777,216(2^{24})
\]
Unicast vs. Multicast

- Requires 3x Bandwidth in this example
- 3X Infrastructure $$$

**Point-to-Point Multiple Streams**

**Multicast One Output Stream**
Multicast Path Establishment via Internet Gateway Management Protocol – IGMP
IGMP Packet Structure

<table>
<thead>
<tr>
<th>bit offset</th>
<th>0–3</th>
<th>4</th>
<th>5–7</th>
<th>8–15</th>
<th>16–31</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>Type = 0x11</td>
<td>Max Resp Code</td>
<td></td>
<td>Checksum</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Group Address</td>
</tr>
<tr>
<td>64</td>
<td>Resv</td>
<td>S</td>
<td>QRV</td>
<td>QQIC</td>
<td>Number of Sources (N)</td>
</tr>
<tr>
<td>96</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Source Address [1]</td>
</tr>
<tr>
<td>128</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Source Address [2]</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>...</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Source Address [N]</td>
</tr>
</tbody>
</table>

Group Address: This is the multicast address being queried when sending a Group-Specific or Group-and-Source-Specific Query. The field is zeroed when sending a General Query.
Input LNs Required

**M60MMXUnn**
- Calc. Method:
  - “P” or “M” class
- Phase Voltages
- Phase Currents
- Frequency
- Rate of Change of Frequency - (HzRte)
- SmpRate

**P120MSQInn**
- Calc Method
  - “P” or “M” class
- Sequence Voltages
- Sequence Currents
- LNs Identified as a C37.118 Calculation Type “P” or “M”
- Report rate included in the LN
- Nominal Frequency added to LPHD
Mapping of C37.118 STN and IDCode

- STN and IDCode mapped into “d”escription field of the Label CDC (LPL) of LLN0
- Mapped as:
  - <STN>-<IDCode>
- SmpRate added as a DO in LLN0
PMU to PDC Mapping
Implementation Agreement
C37.118.2 to 90-5 Data Mapping Proposal:
PMU1 STAT Word (C37.118 Type Bitstring)
PMU1 Data
PMU2 STAT Word
PMU2 Data
.......... 
PMUn STAT Word
PMUn Data
PMU Data Organization

- STAT word (16 bit Unsigned Integer) – Semantics from C37.118.2
- Synchrophasors – Float 32 / Polar Format
- Frequency – Float 32
- Rate of Change of Frequency – Float 32
- Analogs – Float 32
- C37.118.2 Binary Status (16 bit Bitstring)
Standardized Dataset Configuration

- Uses the IEC 61850 XML Configuration language
  - SCL Extensions added for the 16 bit – bit strings used in C37.118
    - There is now a C37.118 “Data Type”
- Dataset members can be published via standard registration services
Security Definition in 90-5

• Defines a Secure Hash Algorithm - SHA2 Hash code for message authentication / integrity
• Defines AES as the encryption algorithm
• Identifies / Extends a Key management system
  – RFC 3547 The Group Domain of Interpretation
  – The publisher manages the keys to all subscribers
  – Same key for Hash and Encryption
Security Options as defined in 90-5:

<table>
<thead>
<tr>
<th>Enumerate value</th>
<th>HMAC algorithm</th>
<th>Number of bits</th>
<th>Designation</th>
<th>Mandatory (m), Optional (o)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>None</td>
<td>None</td>
<td>MAC-None</td>
<td>c1</td>
</tr>
<tr>
<td>1</td>
<td>SHA-256</td>
<td>80</td>
<td>HMAC-SHA256-80</td>
<td>m</td>
</tr>
<tr>
<td>2</td>
<td>SHA-256</td>
<td>128</td>
<td>HMAC-SHA256-128</td>
<td>m</td>
</tr>
<tr>
<td>3</td>
<td>SHA-256</td>
<td>256</td>
<td>HMAC-SHA256-256</td>
<td>m</td>
</tr>
<tr>
<td>4</td>
<td>AES-GMAC</td>
<td>64</td>
<td>AES-GMAC-64</td>
<td>m</td>
</tr>
<tr>
<td>5</td>
<td>AES-GMAC</td>
<td>128</td>
<td>AES-GMAC-128</td>
<td>m</td>
</tr>
</tbody>
</table>

c1 – Shall only be used when encryption is also in use.
One-way Compression

- Input values typically cannot be re-produced
Hash Function Concept

- Processes an arbitrary-length message into a fixed-length output
- Typical implementation breaks the message into N blocks and operates on each block in sequence
Cryptographic Hash Concept

Also known as a Hash based Message Authentication Code – **HMAC**
Also called a Message Integrity Code - **MIC**
Example of Hash Outputs from SHA-1*

* SHA-1 now deprecated due to vulnerabilities
Cryptography Basics

For encryption, the plaintext $P$ is transformed into ciphertext $C$ using the key $K$.

Encryption: $E_K(P) = C$

For decryption, the ciphertext $C$ is transformed back into the plaintext $P$ using the key $K$.

Decryption: $D_K(C) = P$
Block Cypher Concept

AES Works on 128 Bit blocks of data
Packet Encryption via: Advanced Encryption Standard (AES)

AES Encryption Package

the Advanced Encryption Standard (AES) encryption package, also known as Rijndael, is a block cipher adopted as an encryption standard by the US government. The National Institute of Standards and Technology (NIST) established the new Advanced Encryption Standard (AES) specification on May 26, 2002.

The AES encryption package is a cryptographic algorithm that can be used to protect electronic data. Specifically, AES is an iterative, symmetric-key block cipher that can use keys of 128, 192, and 256 bits, and encrypts and decrypts data in blocks of 128 bits (16 bytes).

AES is the successor to the older Data Encryption Standard (DES). DES was approved as a Federal standard in 1977 and remained viable until 1998 when a combination of advances in hardware, software, and cryptanalysis theory allowed a DES-encrypted message to be decrypted in 56 hours. Since that time numerous other successful attacks on DES-encrypted data have been made and DES is now considered past its useful lifetime.

The AES algorithm is based on permutations and substitutions. Permutations are rearrangements of data, and substitutions replace one unit of data with another. AES performs permutations and substitutions using several different techniques.

The AES encryption package will certainly become a de facto standard for encrypting all forms of electronic information, replacing DES. AES-encrypted data is unbreakable in the sense that no known cryptanalysis attack can decrypt the AES cipher text without using a brute-force search through all possible 256-bit keys.
AES Step 1 – Substitute Bytes

In the SubBytes step, each byte in the state is replaced with its entry in a fixed 8-bit lookup table, $S$; $b_{ij} = S(a_{ij})$. 
In the ShiftRows step, bytes in each row of the state are shifted cyclically to the left. The number of places each byte is shifted differs for each row.
In the MixColumns step, each column of the state is multiplied with a fixed polynomial $c(x)$. 
AES – Step 4 – Add round Key

In the AddRoundKey step, each byte of the state is combined with a byte of the round subkey using the XOR operation ($\oplus$).
Group Domain of Interpretation - GDOI

- Publishers act as Controllers
- Receiving Group Members “Pull” new keys
- Centralized Authorization Management
Publisher-based Key Management

Keys are dynamically managed
Changed when a Subscriber is removed
Insight in IEC 61850

What is it?

Ralph Mackiewicz and Herb Falk
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Fax: +1-586-254-0053
Email: ralph@sisconet.com – herb@sisconet.com
Topics

- IEC 61850 and Its Relationship to other IEC Standards
- Impact of IEC 61850 on the Engineering Processes of SCADA, Automation, and Protection
- IEC 61850 Profiles and use of networks
- IEC 61850 Modeling
  - Device and Object Models
  - Service/Behavior Models
- Substation Configuration Language
IEC 61850 and Other Key IEC Standards

What is it with all these “models”?
Definitions:

Interoperability and Integration

The ability of computer systems to exchange information with other systems and to cooperatively implement a useful process or function for the system owner/user.
Interoperability and Integration

- Easy to Achieve:

Nearly anything is possible with enough money and development effort
A Better Way

- Interoperability and Integration without having to program it all yourself:
  - Where applications and devices are inherently capable of interoperating with other systems and performing integrated application functions in a cooperative and distributed manner.

- This is only possible if there are standards to enable it.
  - This work is progressing.

- This is the goal of the IEC TC57 standards
Standards are Needed

- Standards enable independent developers to reduce the uniqueness of their solutions at the interface points

- Traditionally, this meant the protocols to the devices

- But, this did not address the integration dilemma for applications (not devices) that did not use “protocols” as the interface

- Existing protocols provided interoperability but little integration

- A new level of standardization is needed

- This is what the IEC TC 57 Standards are about.
Strategic Vision for Integration and Interoperability

- Abstract Modeling
  - Object and Information Models
  - Abstract Service and Interface Models
  - Self Description and Discovery
  - Technology Independent Design

- Security
  - Applying mainstream standards to TC57 standards
  - Power system specific applications and recommendations
TC 57 Architecture

IEC 61850

ICCP TASE.2
IEC TC 57 Technical Committees

- **WG 3** – Telecontrol Protocols
  - **IEC 60870-5-7** – Security for IEC 60870-5 protocols.

- **WG 10** – Power system IED communication and associated data models
  - **IEC 61850** – Communications for power system automation

- **WG 13** – Energy management system application program interface (EMS - API)
  - **IEC 61970** – Common Information Model (CIM) and Generic Interface Definition (GID)

- **WG 14** – System interfaces for distribution management (SIDM)
  - **IEC 61968** – CIM for distribution and model driven messaging

- **WG 15** – Data and communication security
  - **IEC 62351** – Communications Security

- **WG 16** – Deregulated energy market communications
  - **IEC 62325** – CIM for energy markets

- **WG 17** – Communications Systems for Distributed Energy Resources (DER)
  - **IEC 61850-7-420** – IEC 61850 for DER applications
  - **IEC 61850-8-2** – Web service mapping for IEC 61850

- **WG 18** – Hydroelectric power plants - Communication for monitoring and control
  - **IEC 61850-7-410** – IEC 61850 for Hydropower applications

- **WG 19** – Interoperability within TC 57 in the long term
  - **IEC 62445** – Use of IEC 61850 between control centers and substations
  - IEC 61850-CIM harmonization, ICCP-TASE.2 update, naming and design rules for CIM, quality codes.

- **WG 9** and **WG 20** – Power line carrier systems for DMS (9) and planning for same (20)
  - **IEC 60495, 60663, 62488** – Power line carrier systems

- **WG 21** – Interoperability within TC 57 in the long term
  - **IEC 62746** – Interfaces and protocol for systems connected to the Smart Grid

- **JWG 16** – DLMS/COSEM – metering protocols (TC13)

- **JWG 25** – Windpower systems (TC 88)
Traditional Protocol Standards

- Specified how you arrange bytes on the wire in order to transfer bytes of data between a device and an application

- **Good News: It worked!** Device communications costs were lowered.

- **Bad News:** No standard for data representation or how devices should look and behave to network applications.
  - Some Interoperability but not Integration
Typical Legacy Protocol Data Model

I need the Phase A voltage for the 345KV primary feeder. It is in: Object #6, Variation #2, Index #27. That’s intuitive?

NO POWER SYSTEM CONTEXT FOR DATA ACCESS
Behavior Modeling

- Assume Index #25 is always used to store breaker status.
  - Does 1 mean open or closed?
  - Can I write this object to operate the breaker?
  - Where is the select?
  - Is it selected?

- Even if every device used Index #25 to hold breaker status this still isn’t enough to provide integration.
A New Approach Needed

- For protocols to provide interoperability at the system level they need to:
  - Specify the bytes/format of the data on the wire
  - Specify the meaning of data
  - Specify the behavior of the data
IEC 61850: New Approach

IEC 61850 is a new and innovative approach to substation automation:

- Standardized Device and Object Modeling
  - Logical Devices, Logical Nodes, Common Data Classes, etc.
  - Extensions unique to specific applications (Hydro, Distributed Energy Resources (DER), Wind power, etc.)
- Standardized Service/Behavior Modeling
- Standardized XML for Systems and Device Configuration
- Standardized Communications Protocols for Specific Use Cases:
  - Station Level Monitoring and Control (substation SCADA) (TCP/IP)
  - Protection and Control – GOOSE over Ethernet
  - Sampled Values – Process Bus over Ethernet
- Standardized Conformance Test Cases
Legacy data objects must be mapped to power system for each different device, application, and vendor.

**Power System Functions**

- **Measurements**
  - Phase A Voltage
  - Phase B Voltage
  - Phase C Voltage

- **Controls**
  - Local/Remote Status
  - Breaker Position
  - Blocked Open

- **Protection**
  - Activate Phase A
  - Activate Phase B
  - Activate Phase C

- **Legacy Device**
  - R400040
  - R400041
  - R400042
  - R400043
  - R400044
  - R400045
  - R400046
  - R400047
  - R400048
  - R400049
  - R40004A
  - R40004B

**All Cost…No Value**
IEC61850 Object Model

IED:Relay1/MMXU1.MX.A

IED:Relay1/XCBR2.CO.Pos

Current Measurements

Breaker Position Control

IEC 61850 Object Names
Use Power System Context

Logical Nodes

MMXU1 Measurement Unit #1

XCBR2 Circuit Breaker #2

Logical Device (e.g. Relay1)

Physical Device – Named IED
(network address)
IEC 61850 Object Mapping

- **NO MANUAL MAPPING NEEDED**: IEC61850 objects already portray the power system context.
IEC61850 Models Independent of Function and Brand

**Brand X**
- IOC Relay
- PIOC
  - ST
  - DC
  - Mod
  - Mod
- Measurements **MMXU1**
  - DC
  - MX
  - PhV

**Brand Y**
- Diff Relay
- PDIF
  - ST
  - DC
  - Mod
  - Mod
- Measurements **MMXU1**
  - DC
  - MX
  - PhV

**MMXU1.MX.PhV**
IEC61850 Name for Phase-to-Ground Voltage Measurements
IEC61850 – Layered Standard

<table>
<thead>
<tr>
<th>Device Object Models - IEC61850-7-3, 7-4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Device Model</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Abstract Service Model - IEC61850-7-2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Associate</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Mapping to MMS Protocol - IEC61850-8-1</th>
</tr>
</thead>
<tbody>
<tr>
<td>Initiate</td>
</tr>
</tbody>
</table>

TCP/IP

Ethernet
Benefits of Abstraction and Layering

- Abstract models are independent of the protocol and can be used outside of protocol applications (SCADA tag naming convention)

- Enables definition beyond just the bytes on the wire to incorporate naming and behavior

- Each layer can be optimized independently

- Enables protocols to be separated from application functions to enable use of existing standards (Ethernet, TCP/IP, etc.)

- Enables use of the abstract concepts to other protocols/systems in the future as technology changes.
Other Mappings Possible

<table>
<thead>
<tr>
<th>Abstract Service Model - IEC61850-7-2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Associate</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Device Object Models - IEC61850-7-3, 7-4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Device Model</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Mapping to Web Services – IEC 61850-8-2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Web Services – http/SOAP??</td>
</tr>
</tbody>
</table>

TCP / IP protocol stack
What is ICCP?

The Intercontrol Center Communications Protocol (ICCP) is an international standard (IEC60870-6 TASE.2) for the real-time exchange of SCADA data between control centers and external organizations like other utilities, power pools, and power plants.
Suggested Applicability

- **ICCP** Bilateral Configuration
- Power Plants
- Power Plants
- ISO/RTO/Power Pool
- Neighbor Utility
- Control Center
- Substation
- Substation
- Substation
- Substation

**External Links Only**

- Power System Automation & Protection Internal to Company

**IEC61850 – Automated Configuration**
Common Information Model (CIM) is an object-oriented information model of the power system.

UML – Unified Modeling Language
CIM Packages

IEC 61970 from IEC TC57 WG13
- Core
- Load
- Outage
- Generation
- Domain
- SCADA
- Protection
- Wires
- Measurements
- Topology

IEC 61968 from IEC TC57 WG14
- Assets (metering)
- Documentation
- Consumer
- Core2
- ERP
- OAG

IEC 62325 from IEC TC57 WG16
- Financial
- Energy Scheduling
- Reservation
- Market Operations

Distribution
EMS, Transmission & Planning
Markets (Euro & NA)
The Legacy Data Model Has Always Worked

Applications use Tags

Why do I want the “complexity” of the CIM and all this modeling stuff? Tags are simple!
The Problem to be Solved?

A “one-off” point to point link will always be cheaper if the cost to integrate in the future is ignored.
Actual Business Process From a Real Company

[Diagram showing various processes and departments such as ABC, AP, Budget Analysis, Cash Receipts, Cash Over, Credit, Merchandise Analysis, EDI, Export Orders, Finance, General Ledger, In-Home Repair, Invoices, Management, Merchandise, Operations, Order Fulfillment, Purchasing, Sales Receipts, Sales Support, Sign System, Supply Chain, Vendor Management, and more.]

Prepared by Michelle Mills
Scale The Legacy Data Model Up to the Smart Grid

If it takes 15 seconds to configure a tag, it would take 26 man-years to configure 2M meters!
## CIM versus IEC 61850: What they define

<table>
<thead>
<tr>
<th>CIM</th>
<th>IEC 61850</th>
</tr>
</thead>
<tbody>
<tr>
<td>Detailed Power System Topology</td>
<td>Power System Topology Model</td>
</tr>
<tr>
<td>Asset Model</td>
<td>Device Configuration Description</td>
</tr>
<tr>
<td>Consumer and load models</td>
<td>Device Models</td>
</tr>
<tr>
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<td>Service Models</td>
</tr>
<tr>
<td>Scheduling and transactions</td>
<td>Reporting</td>
</tr>
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<td>Controls</td>
</tr>
<tr>
<td>Work management</td>
<td>Protection</td>
</tr>
<tr>
<td>SCADA and Measurements</td>
<td>Performance/Requirements</td>
</tr>
<tr>
<td>GIS – Location</td>
<td>Object and Data Naming Conventions</td>
</tr>
<tr>
<td>Business Messaging (WG14)</td>
<td>Protocols</td>
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<td></td>
</tr>
</tbody>
</table>
CIM Asset-Power System Models & IEC 61850 Device Models

IEC61970/68 CIM
- Power System Models
- Asset, trading, etc.

IEC61850
- Power System Models
- Device Models

Measurements

WG19 Harmonization
CIM Based Modeling Tool
IEC 61850 Based Modeling Tool (SCL)

Logical Node Designators
Two Different Purposes – Two Solutions are OK, BUT

- Detailed system wide description
- Model exchange for high-level systems
- Power flow, state estimation, etc.
- Market operations
- Planning and system design

- Substation design and modeling
- Device configuration management
- Protection and device control
- SCADA, protection, & control data exchange
CIM and IEC 61850 Difference in Topology

IEC 61970-301 EMS Diagram

IEC 61850-6 SCL Diagram

Are these the same objects?
Simplified Planning Process

- Well defined processes and tools for designing new power system extensions, simulating their impact, defining new contingencies, etc.
Moving Design to Operations

- CIM has improved the ability to move models from planning to operations (and vice-a-versa) in a multi-vendor environment.
  - To be expected through use of standards.
  - Eventually enable wide exchange of planning models like ENTSO-E

- EMS and planning already use a set of tools that have been harmonized to enable the flow of information between them.
Substation engineers use a completely different set of tools supporting a completely different set of standards to define the substation automation and protection systems.
It’s About Productivity

- The effort and knowledge put into the planning and operations models that isn’t embodied in the one-line diagrams is lost and has to be transferred manually into the substation design through the engineering process duplicating previous effort.

- If the tools used a common set of standards the flow of information can be automated enabling topology, SCADA, protection, communications, settings, etc. to be preserved and leveraged through the engineering process.
The Engineering Process of IEC 61850-4

Beyond the protocol to dramatically improve the effectiveness of designing, building and testing power automation systems.
Why is IEC 61850 Different?

If adapted fully from engineering to operations, IEC 61850 (and the model-driven) approach is a new process for power system automation and engineering that is designed to dramatically improve the productivity of engineering, implementation, and maintenance of power automation systems.
The IEC 61850-4 Engineering Process

It starts with a power system design
The IEC 61850-4 Engineering Process

The automation and protection functions are defined
The IEC 61850-4 Engineering Process

IEDs are selected and configuration and programming is defined
The IEC 61850-4 Engineering Process

The IEDs are tied together via the network to integrate the power system automation and protection functions.
The IEC 61850-4 Engineering Process

Monitoring and visualization enables control of the power system
The IEC 61850-4 Engineering Process

The IEC 61850 addresses all aspects of the engineering process that need to be successfully planned, designed, and deployed.
61850 Impacted by all areas of “construction”

The set of utility processes must lead to appropriate:

Power System Resource Selection
61850 Impacted by all areas of “construction”

The set of utility processes must lead to appropriate:

Power System Resource Selection

Power System Topology Design
Generates One-lines
Rating Information
61850 Impacted by all areas of “construction”

The set of utility processes must lead to appropriate:

- Power System Resource Selection
- Power System Topology Design
- IED Selection
- Automation and Protection Logic
61850 Impacted by all areas of “construction”

The set of utility processes must lead to appropriate:

- Power System Resource Selection
- Power System Topology Design
- IED Selection
- Substation design and wiring
61850 Impacted by all areas of “construction”

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- Power System Resource Selection
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- Communications
61850 Impacted by all areas of “construction”

The set of utility processes must lead to appropriate:

- Power System Resource Selection
- Power System Topology Design
- IED Selection
- Substation design and wiring
- Communications

Parameterization of IEDs

Domain of 61850 Substation Configuration Files (SCL) and IEC 61850-6 processes
SCL facilitates specification

IED Specifications

System Specification Description’ (SSD)

System Specification Description (SSD)

Requirements
SCL facilitates design and engineering

System Specification Description (SSD) + IED Selection + Specifics → System Configuration Description (SCD)

Knowledge + Detailed + Parameterization → Configured Device
SCL facilitates validation

Original System Configuration Description (SCD) file

Substation specific System Configuration Description (SCD) file

Configured IED Description (CID) file

Substation Configuration Tool

Configured Device
Vendor Specific Tooling
SCL facilitates testing

System Configuration Description (SCD) file

Configured IED Description (CID) file

Partial SAT

Communication testing Partial FAT

Partial Unit Testing
IEC 61850 Profiles

How IEC 61850 Works Over Networks.
Some Terms

- **Network Access Methods:**
  - Master Slave – a master controls slave access to the network (e.g. DNP3)
  - Peer-to-peer – any entity may send data to any other peer entity on the network without having to coordinate with a master (TCP/IP-Ethernet).

- **Client-Server** – defines roles between 2 peers that communicate directly with just each other on a network.

- **Multicast** – A server with data publishes data to the network which delivers the data to subscribing clients without requiring knowledge of the clients to the publishing server.
Peer – to – Peer communications. Either entity may initiate communications to the other entity.
Multi-Cast Application Association

Service: send Data (unconfirmed)
IEC 61850 Profiles

Multicast
- Measurement Messaging
  - Sampled Values (SV)
  - UDP/IP IGMP/DSCP (90-5)

Client/Server
- Protection Messaging
  - GOOSE
- Symmetric Key Access For 90-5
- Core ACSI Services
- Time Synch
  - IEEE 1588

- GDOI (90-5)
- MMS Protocol
- RFC 1006
- TLS
- TCP/IP
- ISO/IEC 8802-3 Ethertype - Ethernet
Redundant Port Implementations

**Redundant Port:** 2 independent Ethernet ports with 2 different addresses

- MAC – 1  
  IP Addr - 1

- Ethernet1

- MAC – 2  
  IP Addr - 2

- Ethernet2

**Redundant Media:** 1 Ethernet port with switched media

- MAC – 1  
  IP Addr - 1

- Ethernet

Switches on loss of Ethernet link pulses

Primary  Back-Up

Redundant Media is Common - Easy to Configure for Redundancy
Redundant Network Configuration

Rapid Spanning Tree Protocol (RSTP) enables network rebuild after MAC failure/change
HSR – High-Speed Redundancy Ethernet uses this kind of approach to avoid the delay of rebuilding the MAC tables on a failure.
Emerging Approach – Parallel Redundancy Protocol (PRP)

Send to both

PDU

PRP Header

PRP Cache

PDU

First PRP frame in is delivered

LAN A

LAN B
VLANs

- VLANs: Are logical groupings of nodes that reside in a common broadcast domain
  - Virtual because the VLAN is artificially created and the nodes need not be physically located on the same switch or even reside in the same building, but
    - Nodes that are members behave like they are connected together by one layer 2 bridge or switch
    - A router is required to communicate between the two VLANs
Ethernet Priority

- Ethernet 802.1q provides a priority setting
- "High" priority messages are moved to the priority queue
- Specified in IEC GOOSE and Implemented in GE Multilink Switch

Ethernet Switch

Port 5

Port 6

New

Msg 1

Msg 2

Msg 3

Msg 4

15 μsec

Port 1

Port 2

Port 3

Port 4

New “high priority” message for Port 6

Courtesy of GE Digital Energy
IEC 61850 Models
IEC61850 Substation Architecture

Station Bus 10/100/1000 MB Ethernet

Relay(s) Subscribe to Datasets

MU Publishes V/I/Status Datasets

Process Bus .1/1/10GB Ethernet

Remote Access

Network

MU = Merging Unit
IEC61850 – Communications Parts

- Part 6: Substation Configuration Language (SCL)
- Part 7-2: Abstract Communications Service Interface (ACSI) and base types
- Part 7-3: Common Data Classes (CDC)
- Part 7-4: Logical Nodes (LN)
- Part 7-4XX: Other LNs and CDCs
- Part 8-1: Specific Communications Service Mappings (SCSM) - MMS & Ethernet
- Part 9-2: SCSM - Sampled Values over Ethernet
- Part 10: Conformance Testing
IEC 61850 Virtual Model

From IEC61850-7-1
Logical Node

- A named grouping of data and associated services that is logically related to some power system function.

- Consists of one or more attributes each of a type defined by a Common Data Class (CDC)
Examples of Logical Nodes

- Breaker
- Breaker
- Breaker
- Current Transformer
- Voltage Transformer
- Switch Controller
Common Data Classes (CDC)

- Defines structure for common types that are used to describe DATA objects.
- CDC are complex objects built on predefined simple base types organized into functional constraints (FC)

Examples:
- Single point status (SPS) – on/off
- Double point status (DPS) – on/off/transient
- Double point controllable (DPC) – state can be changed via controls
## Common Data Classes - Status

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPS</td>
<td>Single Point Status</td>
</tr>
<tr>
<td>DPS</td>
<td>Double Point Status</td>
</tr>
<tr>
<td>INS</td>
<td>Integer Status</td>
</tr>
<tr>
<td>ENS</td>
<td>Enumerated Status</td>
</tr>
<tr>
<td>ACT</td>
<td>Protection Activation</td>
</tr>
<tr>
<td>ACD</td>
<td>Directional Protection Activation Info.</td>
</tr>
<tr>
<td>SEC</td>
<td>Security Violation Counting</td>
</tr>
<tr>
<td>BCR</td>
<td>Binary Counter Reading</td>
</tr>
<tr>
<td>HST</td>
<td>Histogram</td>
</tr>
<tr>
<td>VSS</td>
<td>Visible String Status</td>
</tr>
</tbody>
</table>
## Common Data Classes - Measurands

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>MV</td>
<td>Measured Value</td>
</tr>
<tr>
<td>CMV</td>
<td>Complex Measured Value</td>
</tr>
<tr>
<td>SAV</td>
<td>Sampled Value</td>
</tr>
<tr>
<td>WYE</td>
<td>Phase to ground measured values for 3-phase system</td>
</tr>
<tr>
<td>DEL</td>
<td>Phase to phase measured values for 3-phase system</td>
</tr>
<tr>
<td>SEQ</td>
<td>Sequence</td>
</tr>
<tr>
<td>HMV</td>
<td>Harmonic value</td>
</tr>
<tr>
<td>HWYE</td>
<td>Harmonic value for WYE</td>
</tr>
<tr>
<td>HDEL</td>
<td>Harmonic value for DEL</td>
</tr>
</tbody>
</table>
## Common Data Classes - Controls

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPC</td>
<td>Controllable Single Point</td>
</tr>
<tr>
<td>DPC</td>
<td>Controllable Double Point</td>
</tr>
<tr>
<td>INC</td>
<td>Controllable Integer Status</td>
</tr>
<tr>
<td>ENC</td>
<td>Controllable Enumerated Status</td>
</tr>
<tr>
<td>BSC</td>
<td>Binary Controlled Step Position Info.</td>
</tr>
<tr>
<td>ISC</td>
<td>Integer Controlled Step Position Info.</td>
</tr>
<tr>
<td>APC</td>
<td>Controllable Analogue Process Value</td>
</tr>
<tr>
<td>BAC</td>
<td>Binary Controlled Analog Process Value</td>
</tr>
</tbody>
</table>
### Common Data Classes – Settings and Descriptions

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SPG</td>
<td>Single Point Setting</td>
</tr>
<tr>
<td>ING</td>
<td>Integer Status Setting</td>
</tr>
<tr>
<td>ENG</td>
<td>Enumerated Status Setting</td>
</tr>
<tr>
<td>ORG</td>
<td>Object Reference Setting</td>
</tr>
<tr>
<td>TSG</td>
<td>Time Setting Group</td>
</tr>
<tr>
<td>CUG</td>
<td>Currency Setting Group</td>
</tr>
<tr>
<td>VSG</td>
<td>Visible String Setting</td>
</tr>
<tr>
<td>ASG</td>
<td>Analogue Setting</td>
</tr>
<tr>
<td>CURVE</td>
<td>Setting Curve</td>
</tr>
<tr>
<td>CSG</td>
<td>Curve Shape Setting</td>
</tr>
<tr>
<td>DPL</td>
<td>Device Name Plate</td>
</tr>
<tr>
<td>LPL</td>
<td>Logical Node Name Plate</td>
</tr>
<tr>
<td>CSD</td>
<td>Curve Shape Description</td>
</tr>
</tbody>
</table>
### Single Point Status (SPS)

**SPS class**

<table>
<thead>
<tr>
<th>Data Attribute name</th>
<th>Type</th>
<th>FC</th>
<th>TrgOp</th>
<th>Value/Value range</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>DataName</td>
<td>Inherited from GenDataObject Class or from GenSubDataObject Class (see IEC 61850-7-2)</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**DataAttribute**

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th>status</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>stVal</td>
<td>BOOLEAN</td>
<td>ST</td>
<td>dchg</td>
<td>TRUE</td>
<td>FALSE</td>
</tr>
<tr>
<td>q</td>
<td>Quality</td>
<td>ST</td>
<td>qchg</td>
<td></td>
<td></td>
</tr>
<tr>
<td>t</td>
<td>TimeStamp</td>
<td>ST</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*substitution and blocked*

<p>| | | | | | |</p>
<table>
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<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>subEna</td>
<td>BOOLEAN</td>
<td>SV</td>
<td></td>
<td></td>
<td>PICS_SUBST</td>
</tr>
<tr>
<td>subVal</td>
<td>BOOLEAN</td>
<td>SV</td>
<td></td>
<td></td>
<td>PICS_SUBST</td>
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<tr>
<td>subQ</td>
<td>Quality</td>
<td>SV</td>
<td></td>
<td></td>
<td>PICS_SUBST</td>
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<tr>
<td>subID</td>
<td>VISIBLE STRING64</td>
<td>SV</td>
<td></td>
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</tr>
<tr>
<td>blkEna</td>
<td>BOOLEAN</td>
<td>BL</td>
<td></td>
<td></td>
<td>O</td>
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</table>

*configuration, description and extension*

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<tbody>
<tr>
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<td>DC</td>
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<td></td>
<td>O</td>
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<tr>
<td>dU</td>
<td>UNICODE STRING255</td>
<td>DC</td>
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<td>O</td>
</tr>
<tr>
<td>cdcNs</td>
<td>VISIBLE STRING255</td>
<td>EX</td>
<td></td>
<td></td>
<td>AC_DLNDNA_M</td>
</tr>
<tr>
<td>cdcName</td>
<td>VISIBLE STRING255</td>
<td>EX</td>
<td></td>
<td></td>
<td>AC_DLNDNA_M</td>
</tr>
<tr>
<td>dataNs</td>
<td>VISIBLE STRING255</td>
<td>EX</td>
<td></td>
<td></td>
<td>AC_DLNDN_M</td>
</tr>
</tbody>
</table>
Functional Constraints

- There are many data attributes in an object like a breaker that have related use:
  - Control, configuration, measurement, reporting, etc.

- Functional Constraints (FC) is a property of a data attribute that characterizes the specific use of the attribute.

- Useful to functionally organize data attributes to provide structure and context.
Mandatory/Optional Parameters

- Specifies whether a data attribute is mandatory (M) or optional (O) and under what conditions.
  - PICS_XXXX: Specifies an ACSI service model (e.g. SUBST = Substitution) that if supported then it is mandatory.
  - AC_xxx specifies if the name space differs from that specified for higher-level objects (e.g. private extensions)
Double Point Status (DPS)

<table>
<thead>
<tr>
<th>DPS class</th>
<th>Data Attribute name</th>
<th>Type</th>
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<th></th>
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<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>stVal</td>
<td>CODED ENUM</td>
<td>ST</td>
<td></td>
<td>dchg</td>
<td>intermediate-state</td>
<td>off</td>
</tr>
<tr>
<td>q</td>
<td>Quality</td>
<td>ST</td>
<td></td>
<td>qchg</td>
<td></td>
<td></td>
</tr>
<tr>
<td>t</td>
<td>TimeStamp</td>
<td>ST</td>
<td></td>
<td></td>
<td></td>
<td>M</td>
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#### substitution and blocked

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#### configuration, description and extension

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2-bit pair in DPS versus boolean in SPS
Controllable Double Point – DPC

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### Control and Status

- **ctlVal**: BOOLEAN, FC: CO, Value: off (FALSE) | on (TRUE), M/O/C: AC_CO_M
- **operTm**: TimeStamp, FC: CO, M/O/C: AC_CO_O
- **origin**: Originator, FC: CO, ST, M/O/C: AC_CO_O
- **ctlNum**: INT8U, FC: CO, ST, Value: 0..255, M/O/C: AC_CO_O
- **stVal**: CODED ENUM, ST, Value: dchg, M/O/C: M
- **q**: Quality, ST, Value: qchg, M/O/C: M
- **t**: TimeStamp, ST, M/O/C: M
- **stSeld**: BOOLEAN, ST, Value: dchg, M/O/C: AC_CO_O

### Substitution

- **subEna**: BOOLEAN, SV, M/O/C: PICS_SUBST
- **subVal**: CODED ENUM, SV, M/O/C: PICS_SUBST
- **subQ**: Quality, SV, M/O/C: PICS_SUBST
- **subID**: VISIBLE STRING64, SV, M/O/C: PICS_SUBST

### Configuration, Description and Extension

- **pulseConfig**: PulseConfig, CF, M/O/C: AC_CO_O
- **ctlModel**: CtlModels, CF, M/O/C: M
- **sboTimeout**: INT32U, CF, M/O/C: AC_CO_O
- **sboClass**: SboClasses, CF, M/O/C: AC_CO_O
- **d**: VISIBLE STRING255, DC, Value: Text, M/O/C: O
- **dU**: UNICODE STRING255, DC, M/O/C: O
- **cddNs**: VISIBLE STRING255, EX, M/O/C: AC_DLNDAD_M
- **cddName**: VISIBLE STRING255, EX, M/O/C: AC_DLNDAD_M
- **dataNs**: VISIBLE STRING255, EX, M/O/C: AC_DLND_M

Optional if control is supported

Mandatory if control is supported

Direct or SBO
Control Model (ctlModel)

- 0: Status only. No control allowed.
- 1: Direct control with normal security
- 2: SBO control with normal security
- 3: Direct control with enhanced security
- 4: SBO control with enhanced security
# Logical Node Name Plate - LPL

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Configuration Revision Parameters

- **configRev** – Changed whenever at least one semantic aspect of the data has changed within the Logical Device (LD) within which this LLN0 is contained. Left to the “user” (vendor) for other LNs.
  - New LNs
  - New attributes.

- **paramRev** – Changed when the value of any editable setting (SE) or setpoint (SP) parameter is changed.
  - If changed via communications or local HMI the value is increased by 1.
  - If changed via SCL import the value is increased by 10,000.

- **valRev** – changed when the value of any configuration (CF) parameter is changed.
  - If changed via communications or local HMI the value is increased by 1.
  - If changed via SCL import the value is increased by 10,000.
## Measured Value - MV

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### DataAttribute

#### measured attributes

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#### substitution and blocked

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#### configuration, description and extension

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</table>
instMag v.s. mag

- Use mag in datasets to trigger a report when data changes
- Use instMag in datasets for reporting data without triggering a report

Deadbanded value. Shall be based on a dead band calculation from instMag as illustrated below. The value of mag shall be updated to the current value of instMag when the value has changed according the configuration parameter db.

NOTE 7 The figure above is an example. There may be other algorithms providing a comparable result; for example as an alternate solution, the dead band calculation may use the integral of the change of instMag. The algorithm used is a local issue.

NOTE 8 This value mag is typically used to create reports for analogue values. Such a report sent "by exception" is not comparable to the transfer of sampled measured values as supported by the CDC SAV.
Trigger Option (TrgOp)

Specifies the conditions under which reporting on the data attribute can be triggered.

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## Sampled Values (SAV)

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**configuration, description and extension**

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<td>rangeAng</td>
<td>ENUMERATED</td>
</tr>
<tr>
<td>q</td>
<td>Quality</td>
</tr>
<tr>
<td>t</td>
<td>TimeStamp</td>
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<tr>
<td>subEna</td>
<td>BOOLEAN</td>
</tr>
<tr>
<td>subCVal</td>
<td>Vector</td>
</tr>
<tr>
<td>subQ</td>
<td>Quality</td>
</tr>
<tr>
<td>subID</td>
<td>VISIBLE STRING64</td>
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<tr>
<td>blkEna</td>
<td>BOOLEAN</td>
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**substitution and blocked**

<table>
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<td>subQ</td>
<td>Quality</td>
<td>PICS_SUBST</td>
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<td>PICS_SUBST</td>
</tr>
<tr>
<td>blkEna</td>
<td>BOOLEAN</td>
<td>O</td>
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</table>

**configuration, description and extension**

| units     | Unit     | CF  | dchg  | see Annex A | |       |
|-----------|----------|-----|-------|-------------||--|--------|
| db        | INT32U   | CF  | dchg  | 0 ... 100 000 | | O |
| dbAng     | INT32U   | CF  | dchg  | 0 ... 100 000 | | O |
| zeroDb    | INT32U   | CF  | dchg  | 0 ... 100 000 | | O |
| rangeC    | RangeConfig | CF | dchg  |          | | GC_CON_range |
| rangeAngC | RangeConfig | CF | dchg  |          | | GC_CON_rangeAng |
| magSVC    | ScaledValueConfig | CF | dchg  |          | | AC_SCAV |
| angSVC    | ScaledValueConfig | CF | dchg  |          | | AC_SCAV |
| angRef    | ENUMERATED | CF  | dchg  | V | A | other | | Synchrophasor |
| smpRate   | INT32U   | CF  | dchg  |          | | O |
| d          | VISIBLE STRING255 | DC  |       | Text      | | O |
| dU         | UNICODE STRING255  | DC  |       |          | | O |
| cdcNs      | VISIBLE STRING255  | EX  |       |          | | AC_DLNDNA_M |
| cdcName    | VISIBLE STRING255  | EX  |       |          | | AC_DLNDNA_M |
| dataNs     | VISIBLE STRING255  | EX  |       |          | | AC_DLNDN_M |

*Vector = mag & ang*
## Vector Type Definition

<table>
<thead>
<tr>
<th>Attribute Name</th>
<th>Attribute Type</th>
<th>Value/Value Range</th>
<th>M/O/C</th>
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<tbody>
<tr>
<td>mag</td>
<td>AnalogueValue</td>
<td></td>
<td>M</td>
</tr>
<tr>
<td>ang</td>
<td>AnalogueValue</td>
<td></td>
<td>O</td>
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From IEC61850-7-3
Logical Nodes

Abstract Representation of a Power System Function
Logical Nodes

IEC61850 Clients

IEC61850 Server

Logical Device

Logical Node

Data

Logical Device

Logical Node

Data

Logical Device

Logical Node

Data

Logical Device

Logical Node

Data

Communications Driver

Field Signals

Legacy Device

Process Bus

Client Functions

1 to N Logical Devices
## System Logical Nodes “L”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>LPHD</td>
<td>Physical Device</td>
</tr>
<tr>
<td>LLNO</td>
<td>Common Logical Node MANDATORY</td>
</tr>
<tr>
<td>LCCH</td>
<td>Physical Communications Channel Supervision</td>
</tr>
<tr>
<td>LGOS</td>
<td>GOOSE Subscription</td>
</tr>
<tr>
<td>LTIM</td>
<td>Time Management</td>
</tr>
<tr>
<td>LTMS</td>
<td>Time Master Supervision</td>
</tr>
<tr>
<td>LTRK</td>
<td>Service Tracking</td>
</tr>
</tbody>
</table>
## Automatic Control Logical Nodes “A”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
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<tbody>
<tr>
<td>ANCR</td>
<td>Neutral Current Regulator</td>
</tr>
<tr>
<td>ARCO</td>
<td>Reactive Power Control</td>
</tr>
<tr>
<td>ARIS</td>
<td>Resistor Control</td>
</tr>
<tr>
<td>ATCC</td>
<td>Automatic Tap Changer controller</td>
</tr>
<tr>
<td>AVCO</td>
<td>Voltage Control</td>
</tr>
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</table>
Supervisory Control Logical Nodes “C”

<table>
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<th>Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>CALH</td>
<td>Alarm Handling</td>
</tr>
<tr>
<td>CCGR</td>
<td>Cooling Group Control</td>
</tr>
<tr>
<td>CILO</td>
<td>Interlocking</td>
</tr>
<tr>
<td>CPOW</td>
<td>Point-on-wave switching</td>
</tr>
<tr>
<td>CSWI</td>
<td>Switch Controller</td>
</tr>
<tr>
<td>CSYN</td>
<td>Synchronizer Controller</td>
</tr>
</tbody>
</table>
## Functional Block Logical Nodes “F”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>FCNT</td>
<td>Counter</td>
</tr>
<tr>
<td>FCSD</td>
<td>Curve Shape Description</td>
</tr>
<tr>
<td>FFIL</td>
<td>Generic Filter</td>
</tr>
<tr>
<td>FLIM</td>
<td>Control Function Output Limitation</td>
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<tr>
<td>FPID</td>
<td>PID Regulator</td>
</tr>
<tr>
<td>FRMP</td>
<td>Ramp Function</td>
</tr>
<tr>
<td>FSPT</td>
<td>Set-Point Control Function</td>
</tr>
<tr>
<td>FXOT</td>
<td>Action at Over Threshold</td>
</tr>
<tr>
<td>FXUT</td>
<td>Action at Under Threshold</td>
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</tbody>
</table>
## Generic Function Logical Nodes “G”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>GAPC</td>
<td>Generic Automatic Process Control</td>
</tr>
<tr>
<td>GGPIO</td>
<td>Generic Process I/O</td>
</tr>
<tr>
<td>GLOG</td>
<td>Generic Log</td>
</tr>
<tr>
<td>GSAL</td>
<td>Generic Security Application</td>
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</table>

Use only when there is no other power system related LN that can represent the data.
### Interfacing and Archiving Logical Nodes “I”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>IARC</td>
<td>Archiving</td>
</tr>
<tr>
<td>IHMI</td>
<td>Human Machine Interface</td>
</tr>
<tr>
<td>ITCI</td>
<td>Telecontrol Interface</td>
</tr>
<tr>
<td>ITMI</td>
<td>Telemonitoring Interface</td>
</tr>
<tr>
<td>ISAF</td>
<td>Safety Alarm Function</td>
</tr>
<tr>
<td>ITPC</td>
<td>Teleprotection Communications Interface</td>
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</table>
## Mechanical and Non-Electrical Logical Nodes “K”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>KFAN</td>
<td>Fan</td>
</tr>
<tr>
<td>KFIL</td>
<td>Filter</td>
</tr>
<tr>
<td>KPMP</td>
<td>Pump</td>
</tr>
<tr>
<td>KTNK</td>
<td>Tank</td>
</tr>
<tr>
<td>KVLV</td>
<td>Valve Control</td>
</tr>
</tbody>
</table>
# Metering and Measurement Logical Nodes “M”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>MHAI</td>
<td>Harmonics or interharmonics</td>
</tr>
<tr>
<td>MHAN</td>
<td>Non phase related harmonics or interharmonics</td>
</tr>
<tr>
<td>MMTR</td>
<td>Metering</td>
</tr>
<tr>
<td>MMXN</td>
<td>Non phase related measurements</td>
</tr>
<tr>
<td>MMXU</td>
<td>Measurements</td>
</tr>
<tr>
<td>MSQI</td>
<td>Sequence and Imbalance</td>
</tr>
<tr>
<td>MSTA</td>
<td>Metering-Statistics</td>
</tr>
<tr>
<td>MENV</td>
<td>Environmental Information</td>
</tr>
<tr>
<td>MFLK</td>
<td>Flicker Measurement</td>
</tr>
<tr>
<td>MHYD</td>
<td>Hydrological Information</td>
</tr>
<tr>
<td>MMDS</td>
<td>DC Measurement</td>
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<tr>
<td>MMET</td>
<td>Metrological Information</td>
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</tbody>
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## Protection Logical Nodes “P”

<table>
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<th>Name</th>
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</thead>
<tbody>
<tr>
<td>PDIF</td>
<td>Differential</td>
</tr>
<tr>
<td>PDIR</td>
<td>Direction</td>
</tr>
<tr>
<td>PDIS</td>
<td>Distance</td>
</tr>
<tr>
<td>PDOP</td>
<td>Directional overpower</td>
</tr>
<tr>
<td>PDUP</td>
<td>Directional underpower</td>
</tr>
<tr>
<td>PFRC</td>
<td>Rate of change of frequency</td>
</tr>
<tr>
<td>PHAR</td>
<td>Harmonic restraint</td>
</tr>
<tr>
<td>PHIZ</td>
<td>Ground detector</td>
</tr>
<tr>
<td>PIOC</td>
<td>Instantaneous overcurrent</td>
</tr>
<tr>
<td>PMRI</td>
<td>Motor restart inhibition</td>
</tr>
<tr>
<td>PMSS</td>
<td>Motor starting time supervision</td>
</tr>
<tr>
<td>POPF</td>
<td>Over power factor</td>
</tr>
<tr>
<td>PPAM</td>
<td>Phase angle measuring</td>
</tr>
<tr>
<td>Name</td>
<td>Description</td>
</tr>
<tr>
<td>-------</td>
<td>--------------------------------------------------</td>
</tr>
<tr>
<td>PSCH</td>
<td>Protection scheme</td>
</tr>
<tr>
<td>PSDE</td>
<td>Sensitive directional earth fault</td>
</tr>
<tr>
<td>PTEF</td>
<td>Transient earth fault</td>
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<tr>
<td>PTOC</td>
<td>Time over current</td>
</tr>
<tr>
<td>PTOF</td>
<td>Over frequency</td>
</tr>
<tr>
<td>PTOV</td>
<td>Over voltage</td>
</tr>
<tr>
<td>PTRC</td>
<td>Protection trip conditioning</td>
</tr>
<tr>
<td>PTTR</td>
<td>Thermal overload</td>
</tr>
<tr>
<td>PTUC</td>
<td>Under current</td>
</tr>
<tr>
<td>PTUV</td>
<td>Under voltage</td>
</tr>
<tr>
<td>PVOC</td>
<td>Voltage controlled time over current</td>
</tr>
<tr>
<td>PVPH</td>
<td>Volts per Hz</td>
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<tr>
<td>PZSU</td>
<td>Zero speed or under speed</td>
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### Protection Logical Nodes (cont'd)

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<thead>
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<th>Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>PRTR</td>
<td>Rotor Protection</td>
</tr>
<tr>
<td>PTHF</td>
<td>Thyristor Protection</td>
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<tr>
<td>PUPF</td>
<td>Underpower Factor Protection</td>
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## Power Quality Events Logical Nodes “Q”

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<tbody>
<tr>
<td>QFVR</td>
<td>Frequency Variation</td>
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<tr>
<td>QITR</td>
<td>Current Transient</td>
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<tr>
<td>QIUB</td>
<td>Current Unbalance Variation</td>
</tr>
<tr>
<td>QVTR</td>
<td>Voltage Transient</td>
</tr>
<tr>
<td>QVUB</td>
<td>Voltage Unbalance Variation</td>
</tr>
<tr>
<td>QVVR</td>
<td>Voltage Variation</td>
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### Protection Related Logical Nodes “R”

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<th>Name</th>
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<tbody>
<tr>
<td>RDRE</td>
<td>Disturbance recorder function</td>
</tr>
<tr>
<td>RADR</td>
<td>Disturbance recorder channel analogue</td>
</tr>
<tr>
<td>RBDR</td>
<td>Disturbance recorder channel binary</td>
</tr>
<tr>
<td>RDRS</td>
<td>Disturbance record handling</td>
</tr>
<tr>
<td>RBRF</td>
<td>Breaker failure</td>
</tr>
<tr>
<td>RDIR</td>
<td>Directional element</td>
</tr>
<tr>
<td>RFLO</td>
<td>Fault locator</td>
</tr>
<tr>
<td>RPSB</td>
<td>Power swing detection/blocking</td>
</tr>
<tr>
<td>RREC</td>
<td>Auto reclosing</td>
</tr>
<tr>
<td>RSYN</td>
<td>Synchronism-check or synchronising</td>
</tr>
<tr>
<td>RMXU</td>
<td>Differential Measurements</td>
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## Sensors and Monitoring Logical Nodes “S”

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<thead>
<tr>
<th>Name</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>SARC</td>
<td>Monitoring and diagnostics for arcs</td>
</tr>
<tr>
<td>SIMG</td>
<td>Insulation medium supervision</td>
</tr>
<tr>
<td>SIML</td>
<td>Insulation medium supervision (liquid)</td>
</tr>
<tr>
<td>SPDC</td>
<td>Monitoring and diag. for partial discharges</td>
</tr>
<tr>
<td>SCBR</td>
<td>Circuit Breaker Supervision</td>
</tr>
<tr>
<td>SLTC</td>
<td>Tap Changer Supervision</td>
</tr>
<tr>
<td>SOPM</td>
<td>Supervision of Operating Mechanism</td>
</tr>
<tr>
<td>SPTR</td>
<td>Power Transformer Supervision</td>
</tr>
<tr>
<td>SSWI</td>
<td>Circuit Switch Supervision</td>
</tr>
<tr>
<td>STMP</td>
<td>Temperature Supervision</td>
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<tr>
<td>SVBR</td>
<td>Vibration Supervision</td>
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### Instrument Transformer Logical Nodes “T”

<table>
<thead>
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<th>Name</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>TCTR</td>
<td>Current transformer</td>
</tr>
<tr>
<td>TVTR</td>
<td>Voltage transformer</td>
</tr>
<tr>
<td>TANG</td>
<td>Angle</td>
</tr>
<tr>
<td>TAXD</td>
<td>Axial Displacement</td>
</tr>
<tr>
<td>TDST</td>
<td>Distance</td>
</tr>
<tr>
<td>TFLW</td>
<td>Liquid Flow</td>
</tr>
<tr>
<td>TFRQ</td>
<td>Frequency</td>
</tr>
<tr>
<td>TGSN</td>
<td>Generic Sensor</td>
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<tr>
<td>THUM</td>
<td>Humidity</td>
</tr>
<tr>
<td>TLVL</td>
<td>LMedia Level</td>
</tr>
<tr>
<td>Name</td>
<td>Description</td>
</tr>
<tr>
<td>-------</td>
<td>--------------------------</td>
</tr>
<tr>
<td>TMGF</td>
<td>Magnetic Field</td>
</tr>
<tr>
<td>TMVM</td>
<td>Movement Sensor</td>
</tr>
<tr>
<td>TPOS</td>
<td>Position Indicator</td>
</tr>
<tr>
<td>TPRS</td>
<td>Pressure Sensor</td>
</tr>
<tr>
<td>TRTN</td>
<td>Rotation Transmitter</td>
</tr>
<tr>
<td>TSND</td>
<td>Sound Pressure Sensor</td>
</tr>
<tr>
<td>TTMP</td>
<td>Temperature Sensor</td>
</tr>
<tr>
<td>TTNS</td>
<td>Mechanical Tension/stress</td>
</tr>
<tr>
<td>TVBR</td>
<td>Vibrations Sensor</td>
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<tr>
<td>TWPH</td>
<td>Water Acidity</td>
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### Switchgear Logical Nodes “X”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
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<tbody>
<tr>
<td>XCBR</td>
<td>Circuit Breaker</td>
</tr>
<tr>
<td>XSWI</td>
<td>Circuit Switch</td>
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</table>
### Power Transformer Logical Nodes “Y”

<table>
<thead>
<tr>
<th>Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>YEFN</td>
<td>Earth fault neutralizer</td>
</tr>
<tr>
<td>YLTC</td>
<td>Tap changer</td>
</tr>
<tr>
<td>YPSH</td>
<td>Power shunt</td>
</tr>
<tr>
<td>YPTR</td>
<td>Power transformer</td>
</tr>
<tr>
<td>Name</td>
<td>Description</td>
</tr>
<tr>
<td>--------</td>
<td>--------------------------------------</td>
</tr>
<tr>
<td>ZAXN</td>
<td>Auxiliary network</td>
</tr>
<tr>
<td>ZBAT</td>
<td>Battery</td>
</tr>
<tr>
<td>ZBSH</td>
<td>Bushing</td>
</tr>
<tr>
<td>ZCAB</td>
<td>Power cable</td>
</tr>
<tr>
<td>ZCAP</td>
<td>Capacitor Bank</td>
</tr>
<tr>
<td>ZCON</td>
<td>Converter</td>
</tr>
<tr>
<td>ZGEN</td>
<td>Generator</td>
</tr>
<tr>
<td>ZGIL</td>
<td>Gas insulated line</td>
</tr>
<tr>
<td>ZLIN</td>
<td>Power overhead line</td>
</tr>
<tr>
<td>ZMOT</td>
<td>Motor</td>
</tr>
<tr>
<td>ZREA</td>
<td>Reactor</td>
</tr>
<tr>
<td>ZRRC</td>
<td>Rotating reactive component</td>
</tr>
<tr>
<td>ZSAR</td>
<td>Surge arrestor</td>
</tr>
<tr>
<td>ZTCF</td>
<td>Thyristor controlled frequency converter</td>
</tr>
<tr>
<td>ZTCR</td>
<td>Thyristor controlled reactive component</td>
</tr>
<tr>
<td>ZRES</td>
<td>Resistor</td>
</tr>
<tr>
<td>ZSCR</td>
<td>Semiconductor Controlled Rectifier</td>
</tr>
<tr>
<td>ZSMC</td>
<td>Synchronous Machine</td>
</tr>
</tbody>
</table>
Logical Node Names

- Example for Circuit Breaker:

```
ddd XCBR01
```

- Logical Node Instance #
- Logical Node Name per IEC 61850-7-4 (circuit breaker)
- Optional Application Specific Prefix

- prefix digits + instance digits ≤ 7 (Ed. 1)
- prefix digits + instance digits ≤ 12 (Ed. 2)
Logical Node Classes

An IEC 61850 device must contain LPHD, LLN0, and 1 or more domain specific logical nodes.
## Physical Device - LPHD

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>PhyNam</td>
<td>DPL</td>
<td>Physical device name plate</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>PhyHealth</td>
<td>ENS</td>
<td>Physical device health</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>OutOv</td>
<td>SPS</td>
<td>Output communications buffer overflow</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Proxy</td>
<td>SPS</td>
<td>Indicates if this LN is a proxy</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>InOv</td>
<td>SPS</td>
<td>Input communications buffer overflow</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>NumPwrUp</td>
<td>INS</td>
<td>Number of Power ups</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>WrmStr</td>
<td>INS</td>
<td>Number of Warm Starts</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>WacTrg</td>
<td>INS</td>
<td>Number of watchdog device resets detected</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PwrUp</td>
<td>SPS</td>
<td>Power Up detected</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PwrDn</td>
<td>SPS</td>
<td>Power Down detected</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PwrSupAlm</td>
<td>SPS</td>
<td>External power supply alarm</td>
<td>O</td>
<td></td>
</tr>
</tbody>
</table>

### Controls

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>RsStat</td>
<td>SPC</td>
<td>Reset device statistics</td>
</tr>
<tr>
<td>Sim</td>
<td>SPC</td>
<td>Receive simulated GOOSE or simulated SV</td>
</tr>
</tbody>
</table>
### Common Logical Node Class

<table>
<thead>
<tr>
<th>Attribute Name</th>
<th>Attr. Type</th>
<th>Explanation</th>
<th>T</th>
<th>M/O</th>
</tr>
</thead>
<tbody>
<tr>
<td>LNName</td>
<td></td>
<td>Shall be inherited from Logical-Node Class (see IEC 61850-7-2)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

#### Data

**Mandatory Logical Node Information** *(Shall be inherited by ALL LN but LPHD)*

<table>
<thead>
<tr>
<th>Mod</th>
<th>INC</th>
<th>Mode</th>
<th>M</th>
</tr>
</thead>
<tbody>
<tr>
<td>Beh</td>
<td>INS</td>
<td>Behaviour</td>
<td>M</td>
</tr>
<tr>
<td>Health</td>
<td>INS</td>
<td>Health</td>
<td>M</td>
</tr>
<tr>
<td>NamPlt</td>
<td>LPL</td>
<td>Name plate</td>
<td>M</td>
</tr>
</tbody>
</table>

**Optional Logical Node Information**

<table>
<thead>
<tr>
<th>Loc</th>
<th>SPS</th>
<th>Local operation</th>
<th>O</th>
</tr>
</thead>
<tbody>
<tr>
<td>EEHealth</td>
<td>INS</td>
<td>External equipment health</td>
<td>O</td>
</tr>
<tr>
<td>EEName</td>
<td>DPL</td>
<td>External equipment name plate</td>
<td>O</td>
</tr>
<tr>
<td>OpCntRs</td>
<td>INC</td>
<td>Operation counter resetable</td>
<td>O</td>
</tr>
<tr>
<td>OpCnt</td>
<td>INS</td>
<td>Operation counter</td>
<td>O</td>
</tr>
<tr>
<td>OpTmH</td>
<td>INS</td>
<td>Operation time</td>
<td>O</td>
</tr>
</tbody>
</table>

ALL other logical nodes contain these attributes even though they are not listed in the other logical node description tables.
# Common Logical Node – LLN0

(Logical Node Zero)

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Data objects</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LocKey</td>
<td>SPS</td>
<td>Local operation for complete logical device</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>LocSta</td>
<td>SPC</td>
<td>Switching authority at station level</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Loc</td>
<td>SPS</td>
<td>Local Control Behavior</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>OpTmh</td>
<td>INS</td>
<td>Operation time</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td><strong>Controls</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Diag</td>
<td>SPC</td>
<td>Run Diagnostics</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>LEDRs</td>
<td>SPC</td>
<td>LED reset</td>
<td>T</td>
<td>O</td>
</tr>
<tr>
<td><strong>Settings</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>MitLev</td>
<td>SPG</td>
<td>Select mode of authority for local control (True – control from multiple</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>levels above the selected one is allowed, False – no other control level</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>above allowed)</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

The Mode (Mod) and Local/Remote status of this logical node affects all LNs in that Logical Device
# Logical Node Description – XCBR

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>LName</td>
<td>The name shall be composed of the class name, the LN-Prefix and LN-Instance-ID according to IEC 61850-7-2, Clause 22.</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>LocKey</td>
<td>SPS</td>
<td>Local or remote key (local means without substation automation communication, hardwired direct control)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>LocSta</td>
<td>SPC</td>
<td>Switching authority at station level</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td><strong>Loc</strong></td>
<td><strong>SPS</strong></td>
<td>Local Control Behavior</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>EEHealth</td>
<td>ENS</td>
<td>External equipment health</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>EEName</td>
<td>DPL</td>
<td>External equipment name plate</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>OpCnt</td>
<td>INS</td>
<td>Operation counter</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td><strong>Controls</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Pos</td>
<td>DPC</td>
<td>Switch position</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>BlkOpn</td>
<td>SPC</td>
<td>Block opening</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>BlkCls</td>
<td>SPC</td>
<td>Block closing</td>
<td>M</td>
<td></td>
</tr>
<tr>
<td>ChaMotEEna</td>
<td>SPC</td>
<td>Charger motor enabled</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td><strong>Metered values</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>SumSwARs</td>
<td>BCR</td>
<td>Sum of Switched Amperes, resetable</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td><strong>Status information</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CBOPCap</td>
<td>INS</td>
<td>Circuit breaker operating capability</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>POWCap</td>
<td>INS</td>
<td>Point On Wave switching capability</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>MaxOpCap</td>
<td>INS</td>
<td>Circuit breaker operating capability when fully charged</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Dsc</td>
<td>SPS</td>
<td>Discrepancy</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td><strong>Settings</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CBTmms</td>
<td>ING</td>
<td>Closing Time of breaker</td>
<td>O</td>
<td></td>
</tr>
</tbody>
</table>

- **Data Object Names**
- **Common Data Class**
- **Description**
- **Mandatory/Optional/Conditional**
### Single Point Status (SPS) CDC

(e.g. loc)

<table>
<thead>
<tr>
<th>Attribute Name</th>
<th>Attribute Type</th>
<th>FC</th>
<th>TrgOp</th>
<th>Value/Value Range</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>DataName</td>
<td>Inherited from Data Class (see IEC 61850-7-2)</td>
<td></td>
<td></td>
<td></td>
<td></td>
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</table>

#### DataAttribute

*status*

<table>
<thead>
<tr>
<th>Attribute Name</th>
<th>Attribute Type</th>
<th>FC</th>
<th>TrgOp</th>
<th>Value/Value Range</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>stVal</td>
<td>BOOLEAN</td>
<td>ST</td>
<td>dchg</td>
<td>TRUE</td>
<td>FALSE</td>
</tr>
<tr>
<td>q</td>
<td>Quality</td>
<td>ST</td>
<td>qchg</td>
<td></td>
<td></td>
</tr>
<tr>
<td>t</td>
<td>TimeStamp</td>
<td>ST</td>
<td></td>
<td></td>
<td></td>
</tr>
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</table>

*substitution*

<table>
<thead>
<tr>
<th>Attribute Name</th>
<th>Attribute Type</th>
<th>FC</th>
<th>TrgOp</th>
<th>Value/Value Range</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>subEna</td>
<td>BOOLEAN</td>
<td>SV</td>
<td></td>
<td></td>
<td>PICS_SUBST</td>
</tr>
<tr>
<td>subVal</td>
<td>BOOLEAN</td>
<td>SV</td>
<td></td>
<td>TRUE</td>
<td>FALSE</td>
</tr>
<tr>
<td>subQ</td>
<td>Quality</td>
<td>SV</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>subID</td>
<td>VISIBLE STRING64</td>
<td>SV</td>
<td></td>
<td></td>
<td></td>
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</tbody>
</table>

*configuration, description and extension*

<table>
<thead>
<tr>
<th>Attribute Name</th>
<th>Attribute Type</th>
<th>FC</th>
<th>TrgOp</th>
<th>Text</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>d</td>
<td>VISIBLE STRING255</td>
<td>DC</td>
<td>Text</td>
<td></td>
<td>O</td>
</tr>
<tr>
<td>dU</td>
<td>UNICODE STRING255</td>
<td>DC</td>
<td></td>
<td></td>
<td>O</td>
</tr>
<tr>
<td>cdcNs</td>
<td>VISIBLE STRING255</td>
<td>EX</td>
<td></td>
<td>AC_DLNDM</td>
<td>AC_DLNDM_M</td>
</tr>
<tr>
<td>cdcName</td>
<td>VISIBLE STRING255</td>
<td>EX</td>
<td></td>
<td>AC_DLNDM</td>
<td>AC_DLNDM_M</td>
</tr>
<tr>
<td>dataNs</td>
<td>VISIBLE STRING255</td>
<td>EX</td>
<td></td>
<td>AC_DLN</td>
<td>AC_DLN_M</td>
</tr>
</tbody>
</table>
Object Name for Local/Remote Attribute of XCBR1

XCBR1.ST.Loc.stVal

- Attribute
- Data
- Functional Constraint
- Logical Node
Mapping of Names via 8-1

- Section 8-1 maps the IEC61850 LN and Data Object Names to MMS (ISO9506)

- MMS allows only numbers, letters, "$", and "_" in object names.

- Resulting MMS Object Name:
  
  XCBR1$ST$Loc$stVal
Object Name for Breaker Position Attribute of XCBR1

XCBR1.ST.Pos.stVal

- Logical Node
- Functional Constraint
- Data
- Attribute
## Measurement Unit (MMXU)

### MMXU class

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>LNName</td>
<td></td>
<td>The name shall be composed of the class name, the LN-Prefix and LN-Instance-ID according to IEC 61850-7-2, Clause 22.</td>
<td></td>
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</tr>
</tbody>
</table>

#### Data objects

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>EEHealth</td>
<td>INS</td>
<td>External Equipment Health (external sensor)</td>
<td>O</td>
<td></td>
</tr>
</tbody>
</table>

#### Measured values

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>TotW</td>
<td>MV</td>
<td>Total Active Power (Total P)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>TotVar</td>
<td>MV</td>
<td>Total Reactive Power (Total Q)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>TotVA</td>
<td>MV</td>
<td>Total Apparent Power (Total S)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>TotPF</td>
<td>MV</td>
<td>Average Power factor (Total PF)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Hz</td>
<td>MV</td>
<td>Frequency</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PPV</td>
<td>DEL</td>
<td>Phase to phase voltages (VL1VL2, ...)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PNV</td>
<td>WYE</td>
<td>Phase to neutral voltage</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PhV</td>
<td>WYE</td>
<td>Phase to ground voltages (VL1ER, ...)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>A</td>
<td>WYE</td>
<td>Phase currents (IL1, IL2, IL3)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>W</td>
<td>WYE</td>
<td>Phase active power (P)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>VAr</td>
<td>WYE</td>
<td>Phase reactive power (Q)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>VA</td>
<td>WYE</td>
<td>Phase apparent power (S)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>PF</td>
<td>WYE</td>
<td>Phase power factor</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>Z</td>
<td>WYE</td>
<td>Phase Impedance</td>
<td>O</td>
<td></td>
</tr>
</tbody>
</table>
Object Name for Phase A to Ground Angle Measurement

MMXU1.MX.PhV.PhsA.cVal.ang.f

Logical Node

Functional Constraint (measurements)

Data
## GOOSE Subscription – LGOS

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>LNNName</td>
<td></td>
<td>The name shall be composed of the class name, the LN-Prefix and LN-Instance-ID according to IEC 61850-7-2, Clause 22</td>
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</table>

### Data objects

**Status information**

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>SbsNdsCom</td>
<td>SPS</td>
<td>Subscription needs commissioning</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>SbsSt</td>
<td>SPS</td>
<td>Status of the subscription (True = active, False=not active)</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>SbsSim</td>
<td>SPS</td>
<td>Subscription with simulation</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>LastStNum</td>
<td>INS</td>
<td>Last state number received</td>
<td>O</td>
<td></td>
</tr>
<tr>
<td>ConfRevNum</td>
<td>INS</td>
<td>Expected Configuration revision number</td>
<td>O</td>
<td></td>
</tr>
</tbody>
</table>

**Settings**

<table>
<thead>
<tr>
<th>Data object name</th>
<th>Common data class</th>
<th>Explanation</th>
<th>T</th>
<th>M/O/C</th>
</tr>
</thead>
<tbody>
<tr>
<td>GoCBRef</td>
<td>ORG</td>
<td>Reference to the subscribed GOOSE control block</td>
<td>O</td>
<td></td>
</tr>
</tbody>
</table>
ACSI

Abstract Communications Service Interface

The IEC 61850 Service Model
ACSI
Abstract Communications Service Interface

- Defines a set of Objects
- Defines a set of Services to manipulate and access those objects
- Defines a base set of data types for describing objects
# ACSI Objects and MMS Mapping

<table>
<thead>
<tr>
<th>ACSI Object Class</th>
<th>MMS Object</th>
</tr>
</thead>
<tbody>
<tr>
<td>SERVER class</td>
<td>Virtual Manufacturing Device (VMD)</td>
</tr>
<tr>
<td>LOGICAL DEVICE class</td>
<td>Domain</td>
</tr>
<tr>
<td>LOGICAL NODE class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>DATA class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>DATA-SET class</td>
<td>Named Variable List</td>
</tr>
<tr>
<td>SETTING-GROUP-CONTROL-BLOCK class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>REPORT-CONTROL-BLOCK class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>LOG class</td>
<td>Journal</td>
</tr>
<tr>
<td>LOG-CONTROL-BLOCK class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>GOOSE-CONTROL-BLOCK class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>GSSE-CONTROL-BLOCK class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>CONTROL class</td>
<td>Named Variable</td>
</tr>
<tr>
<td>Files</td>
<td>Files</td>
</tr>
</tbody>
</table>
## Some ACSI Services

<table>
<thead>
<tr>
<th>ACSI Services</th>
<th>MMS Services</th>
</tr>
</thead>
<tbody>
<tr>
<td>LogicalDeviceDirectory</td>
<td>GetNameList</td>
</tr>
<tr>
<td>GetAllData Values</td>
<td>Read</td>
</tr>
<tr>
<td>GetData Values</td>
<td>Read</td>
</tr>
<tr>
<td>SetDataValues</td>
<td>Write</td>
</tr>
<tr>
<td>GetDataDirectory</td>
<td>GetVariableAccessAttributes</td>
</tr>
<tr>
<td>GetDataDefinition</td>
<td>GetVariableAccessAttributes</td>
</tr>
<tr>
<td>GetDataSet Values</td>
<td>Read</td>
</tr>
<tr>
<td>SetDataSet Values</td>
<td>Write</td>
</tr>
<tr>
<td>CreateDataSet</td>
<td>CreateNamedVariableList</td>
</tr>
<tr>
<td>DeleteDataSet</td>
<td>DeleteNamedVariableList</td>
</tr>
<tr>
<td>GetDataSetDirectory</td>
<td>GetVariableAccessAttributes</td>
</tr>
<tr>
<td>Report (Buffered and Unbuffered)</td>
<td>InformationReport</td>
</tr>
<tr>
<td>GetBRCBValues/GetURCBValues</td>
<td>Read</td>
</tr>
<tr>
<td>SetBRCBValues/SetURCBValues</td>
<td>Write</td>
</tr>
<tr>
<td>GetLCBValues</td>
<td>Read</td>
</tr>
<tr>
<td>SetLCBValues</td>
<td>Write</td>
</tr>
<tr>
<td>QueryLogByTime</td>
<td>ReadJournal</td>
</tr>
<tr>
<td>QueryLogAfter</td>
<td>ReadJournal</td>
</tr>
<tr>
<td>GetLogStatusValues</td>
<td>GetJournalStatus</td>
</tr>
<tr>
<td>Select</td>
<td>Read/Write</td>
</tr>
<tr>
<td>SelectWithValue</td>
<td>Read/Write</td>
</tr>
<tr>
<td>Cancel</td>
<td>Write</td>
</tr>
<tr>
<td>Operate</td>
<td>Write</td>
</tr>
<tr>
<td>Command-Termination</td>
<td>Write</td>
</tr>
</tbody>
</table>
Reporting

- Unbuffered Reporting allows clients to receive data from the server without polling but, if the network connection is lost between the client and server, data is lost.
- Buffered reporting enables the server to retain data if connections are lost enabling the client to retrieve ALL the data.
Buffered Report Tracking Service – BST

<table>
<thead>
<tr>
<th>Attribute name</th>
<th>Attribute type</th>
<th>FC</th>
<th>TrgOp</th>
<th>r/w</th>
<th>Value/value range</th>
<th>M/O/C</th>
</tr>
</thead>
</table>
| Shall inherit all the data attributes of the CST CDC
Specific to the BTS

| rptID           | VISIBLE STRING129   | SR | r     | Service parameter ReportIdentifier is mapped to rptID                             | M     |
| rptEna          | BOOLEAN             | SR | r     | Service parameter ReportEnable is mapped to rptEna                                | M     |
| datSet          | ObjectReference     | SR | r     | Service parameter DataSetReference is mapped to datSet                             | M     |
| confRev         | INT32U               | SR | r     | Service parameter ConfigurationRevision is mapped to confRev                      | M     |
| optFlds         | PACKED_LIST          | SR | r     | Service parameter OptionalFields is mapped to optFlds                              | M     |
| bufTm           | INT32U               | SR | r     | Service parameter BufferTime is mapped to bufTm                                   | M     |
| sqNum           | INT32U               | SR | r     | Attribute SqNum of BRCB is mapped to sqNum                                         | M     |
| trgOps          | TriggerConditions   | SR | r     | Service parameter TriggerOptionsEnabled is mapped to trgOps                       | M     |
| intgPd          | INT32U               | SR | r     | Service parameter IntegrityPeriod is mapped to intgPd                              | M     |
| gi              | BOOLEAN             | SR | r     | Service parameter GeneralInterrogation is mapped to go                             | M     |
| purgeBuf        | BOOLEAN             | SR | r     | Service parameter PurgeBuf is mapped to purgeBuf                                  | M     |
| entryID         | EntryID              | SR | r     | Service parameter EntryIdentifier is mapped to entryID                             | M     |
| timeOfEntry     | EntryTime            | SR | r     | Service parameter TimeOfEntry is mapped to timeOfEntry                             | M     |
| resvTms         | INT16                | SR | r     | Service parameter ReserveTimeSecond is mapped to resvTms                           | O     |
Buffered Reporting with GI Example

IEC 61850 Client

- Client enables BRCB
- Communications Terminated
- Client request General-Interrogation
- Communications Reestablished – Client Re-Enables the BRCB

<table>
<thead>
<tr>
<th>SqNum</th>
<th>Message Type</th>
<th>Data</th>
</tr>
</thead>
<tbody>
<tr>
<td>01</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>02</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>03</td>
<td>Integrity, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>04</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>05</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>06</td>
<td>Integrity, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>07</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>08</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>09</td>
<td>Integrity, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>General-interrogation, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>11</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>Data change, &lt;data&gt;</td>
<td></td>
</tr>
</tbody>
</table>

SqNum = 10 flags when the GI was issued by the client to identify data that was reported while disconnected.
Using Modeling to Control Reports

- Requirements:
  - Desire to receive average readings of current measurements on all 3 phases of a feeder and the actual voltage measurement on one phase every 10 minutes.
  - Any under or over voltage or current conditions should be reported immediately.

- Use MSTA for average current measurements

- Use MMXU for actual voltage measurement and range alarms

- Careful selection of attributes (instMag versus mag) and range alarms enable these complex requirements to be handled via client interaction with server.
General Control Model

1. Operate open, \( T, \) "OperatorA"
2. Operate response
3. Report TRUE, \( t, q, dchg \)

Control object (instance of DATA)

- **ctlVal** (the value to be controlled)
- **operTm** (the time when to operate for the TimeActivatedOperate service)
  - **origin** (indicating who issued the service)
  - **ctlNum** (control sequence number)

- **stVal** (the value to be reported)
- **t** (time of change)
- **q** (quality of value)
Direct Control with Normal Security

Client

Oper_req(off)

Oper_rsp+

Report_req(off)

Control Object
SBO Control with Enhanced Security

Client

| SelVal_req (off) | SelVal_rsp+ | Oper_req (off) | Oper_rsp+ | Report_req(int) | Report_req (off) | CmdTerm_req+ |

Control object

Operated device

Select

Activate output

Between

Off

Deactivate output

Activation of the device mechanism

Acquisition of status input from device
GOOSE Messaging

Applications “subscribe” by listening for data **sent** to a given multi-cast **destination** address
IEC61850 Network Architecture

GOOSE - Generic Object Oriented Substation Event (sends data sets)
## GOOSE Message

<table>
<thead>
<tr>
<th>Name</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>gocbRef</td>
<td>GOOSE Control Block</td>
</tr>
<tr>
<td>timeAllowedtoLive</td>
<td>How long to wait for next message (ms)</td>
</tr>
<tr>
<td>dataSet</td>
<td>The name of the data set being sent</td>
</tr>
<tr>
<td>goID</td>
<td>ID of the sending GOOSE application (gocbRef)</td>
</tr>
<tr>
<td>T</td>
<td>Time of state change</td>
</tr>
<tr>
<td>stNum</td>
<td>State Number</td>
</tr>
<tr>
<td>sqNum</td>
<td>Sequence Number</td>
</tr>
<tr>
<td>Simulation</td>
<td>Simulated Data (true/false)</td>
</tr>
<tr>
<td>confRev</td>
<td>Configuration Revision of the GOOSE Control Block (dataset config)</td>
</tr>
<tr>
<td>ndsCom</td>
<td>Needs Commissioning (true/false)</td>
</tr>
<tr>
<td>numDatSetEntries</td>
<td>Number of data set entries in the message</td>
</tr>
<tr>
<td>Data</td>
<td>The data values of the data set in sequence.</td>
</tr>
</tbody>
</table>

Binary encoding of above data

Ethernet Multicast Address Using 802.3 Ethertype or 90-5 for IP Multicast
GOOSE is reliable multicast

Each line below represents a GOOSE message

- Event at $t=0$
- Time increases until steady state of $\sim 1/\text{min}$ is reached
- State change occurs

- State = 1, Seq = 0
- State = 2, Seq = 0
- State = 1, Seq = 6
Sampled Value Messaging

- Unlike GOOSE Messaging, which is event based, SV messaging is stream based
  - Each message contains one or more samples of data taken at a specified sample rate
  - Messages are sent constantly at a sufficient rate to communicate all the samples.
## Sampled Value Message

<table>
<thead>
<tr>
<th>Name</th>
<th>Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>MsvCBRef</td>
<td>Multicast Sampled Value Control Block</td>
</tr>
<tr>
<td>Dataset</td>
<td>The name of the data set being sent</td>
</tr>
<tr>
<td>MsvID</td>
<td>System wide unique ID of the sending application</td>
</tr>
<tr>
<td>SmpCnt</td>
<td>The number of samples in the message</td>
</tr>
<tr>
<td>RefrTm</td>
<td>Time of the first sample</td>
</tr>
<tr>
<td>ConfRev</td>
<td>Configuration Revision of the MSV Control Block (dataset config)</td>
</tr>
<tr>
<td>SmpSynch</td>
<td>Samples are time are time synchronized (true/false)</td>
</tr>
<tr>
<td>SmpRate</td>
<td>Sample Rate</td>
</tr>
<tr>
<td>SmpMod</td>
<td>Sample Mode: samples/period, samples/second, seconds/sample</td>
</tr>
<tr>
<td>Simulation</td>
<td>Simulated data (true/false)</td>
</tr>
<tr>
<td>Sample [1..n]</td>
<td>The sequence of samples (one data set per sample)</td>
</tr>
</tbody>
</table>

**Binary encoding of above data**

Ethernet Multicast Address Using 802.3 Ethertype or 90-5 for IP Multicast
Substation Configuration Language

SCL
IEC 61850-6
SCL – Substation Configuration Language
IEC61850-6

- Description language for communication in electrical substations related to the IEDs.

- XML based language that allows a formal description of
  - Substation automation system and the switchyard and the relation between them
  - IED configuration
  - Support for private extensions
Basic Substation Config. Language (SCL) File Types

- **SSD**: System Specification Description.
  - XML description of the entire system
- **SCD**: Substation Configuration Description.
  - XML description of a single substation.
- **CID**: Configured IED Description.
  - XML configuration for a specific IED.
- **ICD**: IED Capability Description.
  - Description of what is supported by an IED (required for servers).
How SCL Files Work Together

SSD File – Entire System

SCD File #1
Single Substation
Substation #1

CID File for IED #1
CID File for IED #2

CID File for IED #n-1
CID File for IED #n

SCD File #2
Single Substation
Substation #n

CID File for IED #1
CID File for IED #2

CID File for IED #n-1
CID File for IED #n
ICD versus CID Files

ICD File = What an IED is capable of

CID File = Subset of ICD File Actually Used + Substation Specific Configuration Info.

- Subset:
  - Not all logical nodes, control blocks, I/O, etc. supported by the device are used in a system.

- Substation Configuration Info:
  - Report control block presets
  - Static values for location, and other descriptions.

ICD – IED Capability Description
CID – Configured IED Description
SCL Driven Naming

In the substation structure this CBR is identified as =E1Q1QA1
In the substation structure this LN is identified as =E1Q1LD2QA1CSWI2
In the IED (product) structure this LN is identified as -E1Q1SB1LD2CSWI2
In the communication structure this connection is identified as W1E1Q1SB1S1
In the IED structure this LD is identified as -E1Q1SB1LD2
Logical Device and LN Naming = IEDName
Example of SCL

```xml
<?xml version="1.0" encoding="UTF-8" ?>
    xsi:schemaLocation="http://www.iec.ch/61850/2003/SCL SCL.xsd">
    <Header id="SISCO_DEMO_IED" version="1" revision="2" toolID="WordPad" nameStructure="IEDName"/>
    <Communication>
        <SubNetwork name="StationBus1" type="8-MMS">
            <Text/>
            <BitRate unit="b/s">100</BitRate>
            <ConnectedAP iedName="SISCO_IED_" apName="AXS4MMS_IED">
                <Address>
                    <P type="IP">192.168.0.194</P>
                    <P type="IP-SUBNET">255.255.255.0</P>
                    <P type="OSI-TSEL">0001</P>
                    <P type="OSI-SSEL">0001</P>
                    <P type="OSI-PSEL">00000010</P>
                </Address>
            </ConnectedAP>
            <GSE ldInst="CTRL" cbName="ControlDataSet1">
                <Address>
                    <P type="MAC-Address">01-0C-CD-01-F1-04</P>
                    <P type="APPID">0000</P>
                    <P type="VLAN-ID">1</P>
                    <P type="VLAN-PRIORITY">4</P>
                </Address>
            </GSE>
        </SubNetwork>
    </Communication>
</SCL>
```
SCL Applications

- For users to specify IED requirements.
- For vendors to specify IED capabilities.
- Configure IEC61850 clients w/o IEDs.
- Extract IED configuration from power system design tools.
- Export IED configuration to power system design tools and other applications.
Questions - Discussion
Thank You

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Fax: +1-586-254-0053
Email: ralph@sisconet.com
Applications and Industry Activities

Christoph Brunner
Switzerland
## The Contents of IEC 61850, Edition 1

<table>
<thead>
<tr>
<th>System Aspects</th>
<th>Data Models</th>
</tr>
</thead>
<tbody>
<tr>
<td>Part 1: Introduction and Overview</td>
<td>Basic Communication Structure for Substations and Feeder Equipment</td>
</tr>
<tr>
<td>Part 2: Glossary</td>
<td>Part 7-4: Compatible Logical Node Classes and Data Classes</td>
</tr>
<tr>
<td>Part 3: General Requirements</td>
<td>Part 7-3: Common Data Classes</td>
</tr>
<tr>
<td>Part 4: System and Project Management</td>
<td>Mapping to real Comm. Networks (SCSM)</td>
</tr>
<tr>
<td>Part 5: Comm Requirements for Functions and Device Models</td>
<td>Part 8-1: Mapping to MMS and ISO/IEC 8802-3</td>
</tr>
<tr>
<td></td>
<td>Part 9-1: Sampled Values over Serial Unidirectional Multidrop Point-to-Point link</td>
</tr>
<tr>
<td></td>
<td>Part 9-2: Sampled Values over ISO/IEC 8802-3</td>
</tr>
</tbody>
</table>

### Configuration
Part 6: Configuration Language for electrical Substation IED’s

### Testing
Part 10: Conform. Testing
### Who should know what from IEC61850?

<table>
<thead>
<tr>
<th>Role</th>
<th>Part 1</th>
<th>Part 5</th>
<th>Part 7-1</th>
<th>Part 7-2</th>
<th>Part 7-3</th>
<th>Part 7-4</th>
<th>Part 6</th>
<th>Part 8-1, 9-x</th>
</tr>
</thead>
<tbody>
<tr>
<td>Manager</td>
<td><img src="#" alt="Red" /></td>
<td><img src="#" alt="Red" /></td>
<td><img src="#" alt="Bright yellow" /></td>
<td><img src="#" alt="Red" /></td>
<td><img src="#" alt="Red" /></td>
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<tr>
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<tr>
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<tr>
<td>Application E.</td>
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<td>Communication E.</td>
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<td><img src="#" alt="Bright yellow" /></td>
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</tr>
</tbody>
</table>

**Utility**

**Vendor**

**Consultant**

**Others**

- **Important**
- **Partly important**
- **Minor importance**
Content

- Project Examples
- Application Examples
- Industry Activities
Winznäuschachen (Switzerland)

- In operation since 11/2004
- 16 kV Distribution
- 9 Bays
- Comprising Substation Controller, Relays, Engineering according to part 6
Laufenburg (Switzerland)

- First bay in operation since 12/2004
- 380 kV transmission substation
- Main 2 protection from different supplier
- Retrofit
  - Stepwise retrofit of 7 bays
  - Integration of the existing station control system
PGCIL Maharanibagh 400 kV S/S
Case study: Integral Energy (Australia)

- Issued Functional Specification for IEC 61850 based SAS
- Fully duplicated main 1 / main 2 protection scheme
- No physical wires between IEDs in different bays
Integral Energy - Architecture
Integral Energy – Extract specification

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CB 2012-09-21
Content

- Project Examples
- Application Examples
- Industry Activities
Interlocking (1)

EnaCls[QA1] = f(Q6.QE1, Q1.QB1, ...)

EnaCls[Q1] = f(Q6.QE1, Q1.QB1, ...)

EnaCls[Q2] = f(Q6.QE1, Q1.QB1, ...)

EnaCls[Q6] = f(Q6.QE1, Q1.QB1, ...)

QLD Q1

QA1CSWI1
QA1CIL01
QA1XCBR1
QB1CSWI2
QB1CIL02
QB1XSWI2
...

QLD Q2

QA1CSWI1
QA1CIL01
QA1XCBR1
QB1CSWI2
QB1CIL02
QB1XSWI2
...

QLD Q6

QE1CSWI1
QE1CIL01
QE1XSWI1

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CB 2012-09-21
Interlocking (2)

EnaCls[QA1] = f(Q6.QE1, Q1.QB1, …)
Interlocking (3)

f(Q6.QE1, Q1.QB1, ...)

LD Q1

QB1CSWI2

QA1CILO1

QB1CSWI2.Pos.stVal

Q6/QE1CSWI1.Pos.stVal

EnaOpn

EnaCls

Dataset Q6/Interlock

Open Valid FALSE

Q6/QE1CSWI1.Pos.stVal
Q6/QE1CSWI1.Pos.q
Q6/QE1CSWI1.Pos.stSeld

Open Valid FALSE | n
Underfrequency Load Shedding

PTUF1
PTUF2
PTRC

Feeder to trip on stage 2
Feeder to trip on stage 1
Reverse blocking

GOOSE Message
- PDIS.Str
- PTRC.Op

Trigger Breaker Failure
Control HV
Transformer Protection Main 2
Trigger Breaker Failure
Control LV
Transformer Protection Main 1
Busbar Protection 16 kV BB
Trigger Breaker Failure
Reverse blocking
Feeder Control
Feeder Protection
Feeder Control
Feeder Protection
PUTT scheme – Gateway approach

Gateway approach
PUTT scheme – Tunneling approach

Zone 1

Zone 2

Comm Channel

Permit

Trip Brk

&

OR

Protection IED

PDIS1

PSCH

PDIS2

PTRC

Zone 1

Zone 2

Tunneling approach
Content

- Project Examples
- Application Examples
- Industry Activities
Published documents beyond Ed 1

- IEC 61850-7-410 – Hydroelectric power plants – Communication for monitoring and control
- IEC 61850-7-420 – Communications Systems for Distributed Energy Resources (DER)
- IEC 61850-80-1 – Guideline to exchange information from a CDC based data model using IEC 60870-5-101 / -104
New technical reports published

- IEC 61850-90-1: Using IEC 61850 for communication between substations (published)
- IEC 61850-90-5: Using IEC 61850 to transmit synchrophasor information according to IEEE C37.118
WG10 ongoing work (1)

- IEC 61850-90-2: Using IEC 61850 for communication between substations and control center
- IEC 61850-90-3: Using IEC 61850 for condition monitoring
- IEC 61850-90-4: Network engineering guidelines (LAN in substations)
- IEC 61850-90-11: Methodologies for modeling of logics for IEC 61850 based applications
- IEC 61850-90-12: Network engineering guidelines for WAN
- IEC 61850-90-14: Using IEC 61850 for FACTS data modeling
WG10 ongoing work (2)

- IEC 61850-7-5: Use of logical nodes to model applications – generic principles
- IEC 61850-7-500: Use of logical nodes to model applications in substations
  - Explain, how to use the concepts of IEC 61850 to model the applications of a substation automation system
- IEC 61850-100-1: Methodologies for testing of functions in IEC 61850 based systems
- System management
- Alarm handling
- Preparation of UML model for IEC 61850
WG17 ongoing work

- IEC 61850-90-6: Using IEC 61850 for distribution automation
- IEC 61850-90-7: IEC 61850 object models for photovoltaic, storage and other DER inverters
- IEC 61850-90-8: IEC 61850 object models for electrical vehicles
- IEC 61850-90-9: IEC 61850 object models for electrical energy storage systems
- IEC 61850-90-10: Modeling of schedules in IEC 61850
- IEC 61850-90-15: Hierarchical DER system model
- IEC 61850-8-2: Mapping on Web Services
WG18 ongoing work

- IEC 61850-7-510: Hydro Power plants – Modeling concepts and guidelines
- Communication network structure in hydro power plants
- IEC 61850-90-13: Extensions to include models for steam and gas turbines
- Interoperability test for hydro equipment based on IEC 61850
Logic modeling - Functional view of a LN

LN XXXX

F(x) ["core" function]

Parameters

Logic

InRef1...k

BlkRef1...l

1

In1...n

Blk

Outputs

Controls
Logic modeling - Functional view of a LN

F(x) ["core" function]
Testing - Mirroring control information

IED

Control service

Wired output

ctlVal

opRcvd

opOk

tOpOk

tOpOk
Testing - Simulation of messages
Isolation and test

Protection IED

- Pxxx
- LPHD
- PTRC

Merging Unit

- TCTR
- TVTR

Breaker

- XCBR

Sim

TestSet

Samples (Sim=FALSE) → Samples (Sim=TRUE)

TEST

Samples (Sim=FALSE)

TEST-BLOCKED

XCBR.Pos.opOk

XCBR.Pos.tOpOk

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CB 2012-09-21
Parallel Redundancy Protocol (PRP)

SAN = singly attached nodes
DAN = doubly attached nodes
High Availability Seamless Ring (HSR)
Communication network redundancy

Demo CIGRE 2012
Conclusions

- **IEC 61850 today**
  - Interoperability for communication of information in several domains of the power utility automation
  - Framework to facilitate engineering

- **IEC 61850 tomorrow**
  - Improved engineering interoperability to design distributed automation function like protection and control schemes
  - New features supporting the challenges of a Smart Grid communication backbone
Brief Overview of IEEE 1588 Test Results –
Concept and Practical Examples and
Lessons Learned

Mark Adamiak / Farnoosh Rahmatian

NASPI Meeting, October 16, 2012, Atlanta, GA
IEEE 1588 – Precision Time Protocol (PTP) over Ethernet

- Capable of sub-microsecond time sync
- Provides a messaging system to identify the various clock performances
- Operates through messages in the Ethernet data frame
1588 Basic Operation (1-step, 2-step)

Master Clock Time

10am

MS Delay

$\tau_1$

Sync message

$\tau_2$

Follow_Up message containing value of $\tau_1$

$\tau_3$

Delay_Req message

$\tau_4$

Delay_Resp message containing value of $\tau_4$

time

30 min

Slave Clock Time

11am

Data at Slave Clock

$t_2$

$t_1$, $t_2$, $t_3$

$t_1$, $t_2$, $t_3$, $t_4$
Synchronization Details (clauses 6 & 7)

Master clock sends:
1. Sync message
2. Follow_up message

Time at which a Sync message passed the Timestamp Point \( t_1 \)

Timestamp Point
Synchronization Details (continued)

- **IEEE-1588 Code**
- **Network protocol stack & OS**
- **Sync detector & timestamp generator**
- **Physical layer**

**Master clock receives:**
- **Delay Req message**

**Master clock sends:**
- **Delay Resp message**

Time at which a Delay Req message passed the Timestamp Point ($t_4$)

Timestamp Point
Ethernet Message Time Stamping

Hardware-based Time Stamp is applied based on the Rising Edge of the First Bit after the Start of Frame Delimiter
Time Sync Calculations

$$\text{MS\_difference} = \text{offset} + \text{MS\_delay} = t_2 - t_1 = 11:30 - 10:00 = 90 \text{ min}$$

$$\text{SM\_difference} = -\text{offset} + \text{SM\_delay} = t_4 - t_3 = 11:30 - 10:30 = -30 \text{ min}$$

Assuming that MS\_delay = SM\_delay then:

**Offset and Delay Calculations**

Offset = \((\text{MS\_difference} - \text{SM\_difference})/2\)

= \((90 - (-30))/2\) = 60 Min

One\_Way\_Delay = \((\text{MS\_Difference} + \text{SM\_difference})/2\)

= \((90 + (-30))/2\) = 30 min
Announce Message:

- Clock ID
- Grand Master Clock Accuracy
- Grand Master Variance
- Origin Time Stamp (Sec and nsec)
- UTC Offset
- Grand Master Time Source (e.g. – GPS)
Included in the Sync Message:

- Message ID (e.g. – Sync, Follow-up, delay request)
- PTP Version Number
- Message Length (2 bytes)
- Subdomain Number (1 byte)
- PTP Flags (16)
- Correction (nsec)
- Clock Identity (8 bytes)
- Source Port (2 bytes)
- Origin Time Stamp (Seconds & Nanoseconds)
Timing Latency & Fluctuation

Application layer
Network protocol stack
Physical layer

Application layer
Network protocol stack
Physical layer

Repeater, Switch, or Router

Repeaters & Switches: fluctuations ~100ns to usec
Routers: fluctuations ~ms

msecs of delay and fluctuation
< 100 nsecs of delay and fluctuation
Proof of Concept Testing - Architecture

PG&E Synchrophasor Project – Proof of Concept Architecture

C37.118 is used for interim testing while harmonization with IEC 61850 is demonstrated

DAS 1

Ancillary PDC 1

60 datasets/s

Super - PDC 1

EMS 1

1 dataset/s

100Mb Switches

EMS Visualization

External Visualization Client

EMS 2

Super - PDC 2

Ancillary PDC 2

60 datasets/s

DAS 2

Remote Client Test Receiver

CAISO/ WECC Interface

Remote Clients

IEC 61850-based Phasor Gateway

Noise Generator

30/60 datasets/s From SSE - TBD

Communication Network

Engineering Visualization

PMU Emulator

BLUE – C37.118 Via Ethernet

BLACK – Vendor Specific

SSD = Solid State Disk

DFR PMU

PMU 1

PMU 2

PMU 5

PMU 6

PMU 3

PMU 4

PMU 7

PMU 8

GPS Clock

IRIG-B

Test Analogs

PMU Emulator PDC tester

DAS: Data Archival System

PMU: Phasor Measurement Unit

EMS: Electric Management System

PDC: Phasor Data Concentrator

Substate Estimator

Historian

Substation PDC-1

Substation PDC-2

120 phasors/s

1588

1588

100Mb Switches

SSD
Noise Impairment Tests

- Noise Injection / IP Packet interference
Observations – Timing functions (GPS, IRIG-B, and IEEE 1588)

Before calibration (-0.25 μs)

Reference (MIL) clock

~15 minutes after GPS Antenna removal from Grandmaster (10 μs)

After calibration (0.1μs)
Compensating for GPS Antenna cable length

Grandmaster Transparent-Clock

10 μs delay injected in 1588 path between grandmaster and transparent clock
Observations – Timing functions (GPS, IRIG-B, and IEEE 1588)

• Several GPS-synchronized clocks providing timing accuracy better than 1 us (mostly on the order of 0.1 us)
• Some clocks did not update time-quality bits in IRIG-B timing data after loss of GPS input. Similarly, for IEEE 1588 PTP.
• In the absence of GPS input, clock drifts on the order of $10^{-7}$ to $10^{-9}$ were observed from different clocks.
  – Typical commercial products
    - $10^{-9}$ is a drift of 4 us in about an hour
    - $10^{-7}$ is a drift of 26 us in about 4 minutes (Bad Time)
      - Synchrophasor permissible TVE of 1% ~ 26.5 us
• Other 1588 PTP (precision time protocol) test results
  – Typical accuracy of 0.1 to 0.5 us has been observed.
  – Any delay in network communication can translate to delay in Transparent Clock when not compensated.
  – Some Slave clocks assume transmission delay is the same in both directions (usually OK, but not always)
Tests – Summary of the Findings

• Several GPS-synchronized clocks providing timing accuracy better than 1 us (mostly on the order of 0.1 us)

• Some clocks did not update time-quality bits in IRIG-B timing data after loss of GPS input. Similarly, for IEEE 1588 PTP.

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  – Typical accuracy of 0.1 to 0.5 us has been observed.
  – Any delay in network communication can translate to delay in Transparent Clock when not compensated.
  – Some slave clocks assume transmission delay is the same in both directions (usually OK, but not always)
IEC61850 Levels of Time Synchronization

LN – STIM identified to set time and provide time synchronization in a substation

<table>
<thead>
<tr>
<th>Class</th>
<th>Accuracy</th>
<th>Function/phase error</th>
</tr>
</thead>
<tbody>
<tr>
<td>T1</td>
<td>± 1 ms</td>
<td>Event timing</td>
</tr>
<tr>
<td>T2</td>
<td>± 0.1 ms</td>
<td>Zero Crossing / Sync Check</td>
</tr>
<tr>
<td>T3</td>
<td>± 25 μs</td>
<td>32’ at 60Hz / 27’ at 50 Hz</td>
</tr>
<tr>
<td>T4</td>
<td>± 4 μs</td>
<td>5’ at 60Hz / 4’ at 50 Hz</td>
</tr>
<tr>
<td>T5</td>
<td>± 1 μs</td>
<td>1’ - Synchrophasors</td>
</tr>
<tr>
<td>T6</td>
<td>± 0.1 μs</td>
<td>Available, but not defined yet</td>
</tr>
</tbody>
</table>
## C37.118 4 Bit Time Quality Indicator Code

<table>
<thead>
<tr>
<th>BINARY</th>
<th>HEX</th>
<th>VALUE (worst case accuracy)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1111</td>
<td>F</td>
<td>Fault--Clock failure, time not reliable</td>
</tr>
<tr>
<td>1011</td>
<td>B</td>
<td>10 seconds</td>
</tr>
<tr>
<td>1010</td>
<td>A</td>
<td>1 second</td>
</tr>
<tr>
<td>1001</td>
<td>9</td>
<td>100 milliseconds (time within 0.1 sec)</td>
</tr>
<tr>
<td>1000</td>
<td>8</td>
<td>10 milliseconds (time within 0.01 sec)</td>
</tr>
<tr>
<td>0111</td>
<td>7</td>
<td>1 millisecond (time within 0.001 sec)</td>
</tr>
<tr>
<td>0110</td>
<td>6</td>
<td>100 microseconds (time within 10^-4sec)</td>
</tr>
<tr>
<td>0101</td>
<td>5</td>
<td>10 microseconds (time within 10^-5sec)</td>
</tr>
<tr>
<td>0100</td>
<td>4</td>
<td>1 microsecond (time within 10^-6 sec)</td>
</tr>
<tr>
<td>0011</td>
<td>3</td>
<td>100 nanoseconds (time within 10^-7 sec)</td>
</tr>
<tr>
<td>0010</td>
<td>2</td>
<td>10 nanoseconds (time within 10^-8 sec)</td>
</tr>
<tr>
<td>0001</td>
<td>1</td>
<td>1 nanosecond (time within 10^-9 sec)</td>
</tr>
<tr>
<td>0000</td>
<td>0</td>
<td>Normal operation, clock locked</td>
</tr>
</tbody>
</table>

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Interoperability Issues, Tests, and Practical Examples (PMU, PDC, SuperPDC)

Vahid Madani / Farnoosh Rahmatian

NASPI Meeting, October 16, 2012, Atlanta, GA
Interoperability Considerations

- Standards
  - Having standard(s) is required but not sufficient for interoperability.
  - Standard compliance doesn’t guarantee interoperability

- Implementation Agreements
  - Clear Implementation Agreement(s) among various device developers and manufacturers are usually required to achieve interoperability

- Testing
  - Both standards and implementation agreements are subject to interpretation and may include options, choices, or configurations.
  - Only actual testing can verify interoperability between various devices/systems.
  - Testing often identifies the need (or desire) for improvements and enhancements, as well as feedback for improving standards and implementation agreements.

- Life-cycle management
  - Life-cycle management, asset utilization, and revision control are all considerations affected by interoperability
  - Device interoperability needs to support system life-cycle management and asset utilization (long-term system deployment roadmap to be supported)
IEC 61850 Certification

- Benefits of conformance test before implementation?
  - Resolves interpretation differences
  - Early identification of non-conformances
  - Exact functionality of the protocol is identified
  - Multi-vendor solutions will have interoperability issues
  - New offerings will have bugs
  - Most manufacturers have missing pieces

- Conformance Test
  - Does implementation conform to the standard?
  - What is the behavior in error situations?

- Interoperability Test
  - Do the communication services provided by one device interoperate with other devices?

- Involves UCA-IUG Documents
  - QA Program
  - Accreditation program
  - Approved Test Procedures

- Requiring a Certified Product from a Vendor Means:
  - Vendor makes the investment as appraisal costs
  - Cost is spread over all sold licenses of the product
  - Early discovery of problems avoids dealing with unexpected behaviors during installation and over project life cycle
  - Vendor and User save dollars and time in Operation & Maintenance
IEC 61850 Certification does not address….

- Interoperability Test
  - Does not cover application interoperability
  - Burden is on the User – Include in your Spec.

- Stress and Performance Test
  - Identifies boundary limits for the device
  - Establishes whether the device meets the performance requirements for the application

- Reliability, Redundancy, Failure Modes and Contingencies
Example – Hybrid GOOSE and Serial Communication

Use of high speed serial links (Up to T1 level) and Interface with GOOSE Messaging
Benefits

- Replacement of wires with communication
  - Costs
  - Improved flexibility
  - Reduced commissioning time
  - Improved monitoring
  - Ease of use with optical sensor technology
  - Prevents mixing DC circuits
  - Ease of applications when I/Os are limited

- Besides SCADA Data
  - Superior Asset Management means
  - Condition monitoring of primary equipment
  - Power Quality Information

- Self description and configuration language
  - Verify the configuration of the system
  - Automatically create test patterns

- Ease of Applications with Synchronized phasors
  - Data sharing over large geographical grids
    - Neighboring systems (subject to Cyber security)
  - Phasor Data Concentrators (PDC)

- When implemented properly...
- Self monitoring capability
  - Constant real-time status of control point communications, with alarming by IEDs for failure anywhere from the source IED intelligence to the implementing IED.

- Fits with “overlapping zones of monitoring” approach to having a maintenance program.
  - Critical for Protection
  - Vital for System Integrity Protection Schemes (SIPS) and Wide-area Remedial Actions Schemes (RAS)

- Better than many or most existing schemes in this way
- Timing and loss rate monitoring can be programmed
Design Requirements/Criteria

Example – typical for the overall system
1. High availability/reliability (>99.995)
2. Automatic self diagnostics
3. Easy to maintain
4. Throughput time < 25 ms
   - Hierarchical structure
   - Detection, transmission, central processing, and execution
5. Full Redundancy (no single point of failure)
6. Controllers-Triple Modular Redundant- (2 out of three voting scheme)
7. Compliance with regulations
Messaged versus Hardwired

• Advantages:
  - Replace local control wiring with network communications
  - No need for periodic testing (devices communicate continuously)
  - Minimizes hardware installation
  - Saves space
  - Provides almost endless expansion capabilities
  - No need to update wiring diagrams
  - No contacts, debounce filtering, not prone to noise

• Drawbacks:
  - Signal Propagation delay – *May be variable*
  - Need robust setting tracking mechanism (version control)
Example - Wide-Area Protection and Control

Controller-A

- OPC
- IEC 61850

Controller-B

- OPC
- IEC 61850

GPS Clock

Substation

Controller Sites

Hub

Watts, VAR, Freq., Temperature, Thermal, Phasor, Angle

MMXU/GGIO Report over TCP/IP Ethernet

SOE OSC

Primary

Alternate

Status & Actions

V/A Status

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Example
Wide-Area Protection and control

LEGEND:
EQ --- Equipment Unavailable
CF --- Channel Fail
LA --- Local Mode
EF --- Ethernet Fail
LL --- Line Loss
OC --- Outage Confirmation
MW --- Megawatts
MV --- Megavars
FQ --- Frequency
Comm --- Communication Fail
Test --- Goose test bit
ED --- RTP Modbus Error Detection
RS --- RTP Modbus Register Status
N1 --- Modbus Node 1
N2 --- Modbus Node 2
N3 --- Modbus Node 3
Benefits of using New Emerging Technologies

- Condition based maintenance (reduction of human errors)
- Performance statistics
- Tools for Clearance analysis and tracking
- Database historian can be leveraged to provide maintenance and testing reports.
- Flexibility in Engineering, Design, and Asset Utilization
- Access operational and non-operational data for many business purposes
- Sequence of Events (SOE) Records-Oscillography
- Trending data
- Help in system restoration
- Automated reports/charts generation
- Ability to record and playback actual events
Why focus on Ethernet control?

- Deployment of advancing IT technology elsewhere in the enterprise – Can we leverage the investment?
- Industrial activity
  - Ethernet LANs for control used in process control
  - Modbus RTU on TCP
  - Ethernet IP – DeviceNet in Ethernet format.
  - Lots of low-cost hardware is breaking down old focus on “non-deterministic” network – not as bad as feared!
  - Sub-microsecond accuracy time sync over Ethernet (IEEE 1588)
- Substation LANs
  - DNP3 over TCP; Modbus TCP
  - UCA and IEC 61850 emerging (more later)
Example: RING or Breaker and Half Bus Messaging
## What is Needed? - Tools

<table>
<thead>
<tr>
<th>Device / Output</th>
<th>REMOTE I/O--RING BUS</th>
<th>Device / Output</th>
<th>REMOTE I/O--RING BUS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.1</td>
<td>1.1 BFT BLOCK AUTO (86BF)</td>
<td>1.1 DEVICE:</td>
<td>1.1 RO:1</td>
</tr>
<tr>
<td>1.3</td>
<td>1.3 BFT TO DTT</td>
<td>1.3 RO:3</td>
<td>1.3 RI:2</td>
</tr>
<tr>
<td>1.2</td>
<td>1.2 HS 1ONLY INTLK TO LEFT</td>
<td>1.2 RO:2</td>
<td></td>
</tr>
<tr>
<td>1.4</td>
<td>1.4 HS 1ONLY INTLK TO RIGHT</td>
<td>1.4 RO:4</td>
<td>1.4 RI:4</td>
</tr>
<tr>
<td>1.5</td>
<td>1.5 CBM CONTROL</td>
<td>1.5 RO:5</td>
<td>1.5 RI:6</td>
</tr>
<tr>
<td>1.7</td>
<td>1.7 CBM LT13 TO LEFT</td>
<td>1.7 RO:7</td>
<td></td>
</tr>
<tr>
<td>1.8</td>
<td>1.8 CBM LT14 TO RIGHT</td>
<td>1.8 RO:8</td>
<td>1.8 RI:9</td>
</tr>
<tr>
<td>1.12</td>
<td>1.12 52A 20n2</td>
<td>1.12 RO:12</td>
<td>1.12 RI:11</td>
</tr>
<tr>
<td>2.8</td>
<td>2.8 STUB BUS STAT</td>
<td>2.8 RO:8</td>
<td>2.8 RI:10</td>
</tr>
<tr>
<td>2.15</td>
<td>2.15 REMOTE BLK CLOSE</td>
<td>2.15 RO:15</td>
<td>2.15 RI:16</td>
</tr>
<tr>
<td>3.1</td>
<td>3.1 BFT BLOCK AUTO (86BF)</td>
<td>3.1 RI:1</td>
<td>3.1 RO:1</td>
</tr>
<tr>
<td>3.3</td>
<td>3.3 BFT TO DTT</td>
<td>3.3 RI:1</td>
<td>3.3 RO:3</td>
</tr>
<tr>
<td>3.2</td>
<td>3.2 HS 1ONLY INTLK TO LEFT</td>
<td>3.2 RI:2</td>
<td>3.2 RO:2</td>
</tr>
<tr>
<td>3.4</td>
<td>3.4 HS 1ONLY INTLK TO RIGHT</td>
<td>3.4 RI:4</td>
<td>3.4 RO:4</td>
</tr>
<tr>
<td>3.5</td>
<td>3.5 CBM CONTROL</td>
<td>3.5 RI:7</td>
<td>3.5 RO:5</td>
</tr>
<tr>
<td>3.7</td>
<td>3.7 CBM LT13 TO LEFT</td>
<td>3.7 RI:8</td>
<td>3.7 RI:8</td>
</tr>
<tr>
<td>3.8</td>
<td>3.8 CBM LT14 TO RIGHT</td>
<td>3.8 RO:8</td>
<td>3.8 RI:9</td>
</tr>
<tr>
<td>3.12</td>
<td>3.12 52A 20n2</td>
<td>3.12 RI:12</td>
<td>3.12 RO:12</td>
</tr>
</tbody>
</table>

### Notes:
- **Set A Line 1**: CB-2012
- **Set A Line 2**: CB-2022
- **Set A Line 3**: CB-2032
- **Set A - XFMR**: Set A - XFMR
What is needed?

- Adequate training of engineers and practitioners
- Adoption to existing substation automation concepts and changes in the specification and design process
- Conformance Certification of Devices to IEC 61850
- The protection engineers need to prepare for the changed specification and design process
- Tools Industry Participation is Key:
  - Application Development
  - Documentation of engineering design – Substitues for Wiring and Schematics
  - Mapping
  - Performance Tracking
  - IP Address Standards and Management - Over Project Life Cycle
  - Ease of expansions
  - Configuration and testing
  - Troubleshooting
  - Maintenance
- Basic understanding of
  - The engineering approach with the use of the configuration language
  - The concepts of the object models and the basic communication services
  - The Ethernet technology with switches and priority tagging
What is Needed: Process Change

- Roll out for substation automation and Protection interlocking
- Top down process for specification and design
- Specification shall concentrate on
  - Functionality and data to be communicated
  - Performance and availability requirements
- Role of system integrator
  - Roles need to be properly defined
  - Responsible for performance and behavior of the overall system
  - Cooperation between user and manufacturer is important
Summary Conclusion

- Balancing Wire vs. Messaging
  - Know your application
  - Minimize logic variations between bus configurations

- Challenges and Opportunities - Bottom line:
  - Requires Teamwork with IT, Automation, and SCADA / EMS groups
  - Requires Acceptance by Management and Operators
  - Automating the system to meet the challenges of 21st century & reliable energy delivery
  - Real-time system prognosis and adaptive protection and controls
  - Reduce Down time
  - Easily adaptable to advancing technology – Ease of upgrade
  - Saving time & Smart Investment - $$$
  - Tools – User input is needed

- Real benefits
  - Innovative protection & control integration and automation solutions

- Internal Challenges and solutions
  - Internal:
    - Develop a migration path
    - Widespread rollout vs. spot upgrade
    - Standards, Training, Management Leadership support, In-house tools
    - When opportunity knocks, follow the blueprint
  - Externals:
    - Industry collaborations for well defined standards
    - Work with vendors to develop features, enhance capability, tools, training
IEC 61850 – Process Level Mappings

- Get rid of most switchyard wiring.
- Streams of sampled values of voltages and currents multiplexed on few Ethernet fibers.
- Status and samples come from switchyard; control and time signals goes out into the yard
- Wiring losses and burdens are cut.
- EMI is eliminated.
- New signals can be added without new wiring to control house.
Interoperability Test – PMU to PDC Communications

• Synchrophasor data transport using IEC 61850-90-5 – streaming data:
  – PMU to Substation PDC
  – Substation PDC to Super PDC
  – PMU to Super PDC
  – Comparison with IEEE C37.118 UDP

• Communicating configuration data

• Mixed protocol – a PDC receiving IEC61850-90-5 and transmitting in IEEE C37.118.2-2011 UDP

• Impact of timing signals (e.g., IEEE 1588 v2 with power profile C37.238-2011)

• Layered testing
  – Can the receiving device parse the received data (protocol)
  – Is the received data correctly understood / interpreted (format)
  – Is the received data correct or corrupted – and consequences (data quality)
    ▪ E.g., misunderstood time-stamp – and what will happen to the data (lost or saved or flagged)
Examples: Comparison of Data Transmission using C37.118 UDP and IEC 61850-90-5 Unicast and Multicast

Set-up at PG&E Proof of Concept (POC) Test facility:
• Two PMUs, labeled 5PMU1 and 5PMU6 see the same voltage and current information
  – 5PMU1 – transmits using C37.118 through a substation PDC to a SuperPDC (SPDC)
  – 5PMU6 – transmits using 61850-90-5 directly to the SPDC
• 1 ASDU / Frame – One-to-one relationship between frame and data units
  – Data unit is same as message size, i.e., 4300 bytes
  – Reminder: ASDU – Application Service Data Units
  – Can also set up to include multiple data units (ASDU) per frame - To allow repeated data to protect against loss of data (address missing data). Not shown in this presentation.
• PMUs are streaming P Class data at 120 records (frames) per seconds
• Interval of tests is 10 second (1200 records / seconds)
Test Setup Progression – Using IEC 61850-90-5

**Test 1** – Test Messages Transmitted By PMU to OpenPDC

**Test 4** – Test On Isolated POC Lab Ethernet Switches With VLAN

**Test 5** – Test On Single Subnet With Router Connected

...
Test Setup Progression

- Unicast and Multicast between PMUs and Test PC
- IEC 61850-90-5 Destination Data Set To Port 102
- **5PMU1** – transmits using C37.118 through a substation PDC to a SuperPDC (SPDC)
- **5PMU6** – transmits using 61850-90-5 directly to the SPDC

**Test 13** - Unicast and Multicast To SPDC Through Firewall
Multicast Between Field and Central Location
Interoperability Tests – Summary of the Findings

• A collaborative testing environment is a significant asset
• Various interpretations of standards and implementation agreements
  – Quality and status flags – limited implementation in some cases
• Different challenges for streamed data (e.g., synchrophasor measurements) and one-time data (e.g., status, control, config data)
• Unicast and multicast implementations
  – Varying support levels for routers, switches, and firewalls within the data path
• Impact of security features on interoperability and testing
  – Access to devices for configuration
  – IGMP 3.0 support
• Awareness of other system factors such as operating systems or communications stacks settings and limitations.
  – Example, UDP and TCP buffer sizes.
• Tools, tools, tools …
  – The need for efficient configuration and testing tools to accelerate and streamline interoperability (and other) tests.